IN

- No Frequency Compensation Required
- Low Power Consumption
- Short-Circuit Protection
- Offset-Voltage Null Capability
- Wide Common-Mode and Differential Voltage Ranges
- No Latch-Up
- Designed to Be Interchangeable With Fairchild μA747C and μA747M

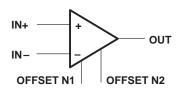
description

The uA747 is a dual general-purpose operational amplifier featuring offset-voltage null capability. Each half is electrically similar to uA741.

The high common-mode input voltage range and the absence of latch-up make this amplifier ideal for voltage-follower applications. The device is short-circuit protected and the internal frequency compensation ensures stability without external components. A low-value potentiometer may be connected between the offset null inputs to null out the offset voltage as shown in Figure 2.

The uA747C is characterized for operation from 0° C to 70° C; the uA747M is characterized for operation over the full military temperature range of -55° C to 125° C.

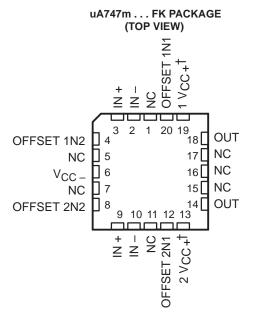
symbol (each amplifier)



(TOP VIEW) 14 OFFSET 1N1 IN + [] 1 V_{CC +}† 2 13 OFFSET 1N2 □ Тυο Π 12 J NC Vcc_[11 10 OUT $IN + \Gamma$ 9 2 V_{CC+}†

OFFSET 2N1

D, J, N, OR W PACKAGE



NC - No internal connection

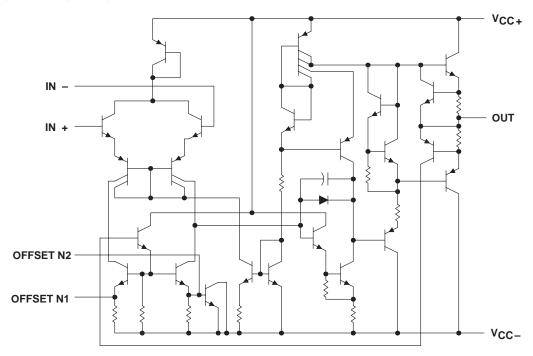
AVAILABLE OPTIONS

		PACKAGE							
	V May		20-PIN						
TA	V _{IO} Max AT 25°C	SMALL OUTLINE (D)	CERAMIC DIP (J)	PLASTIC DIP (N)	FLAT PACK (W)	CHIP CARRIER (FK)			
0°C to	6 mV	uA747CD	_	uA747CN	_	_			
70°C	OTITY	uni 4100		UAT 47 ON					
−55°C to 125°C	5 mV	_	uA747MJ	_	uA747MW	uA747MFK			

The D package is available taped and reeled. Add the suffix R to the device type, (i.e., uA747CDR).

[†] The two positive supply terminals (1 V_{CC +} and 2 V_{CC +}) are connected together internally.

schematic (each amplifier)



absolute maximum ratings over operating free-air temperature range (unless otherwise noted)

		uA747C	uA747M	UNIT		
Supply voltage, V _{CC+} (see Note 1)	18 22					
Supply voltage, V _{CC} (see Note 1)		-18 -22 V				
Differential input voltage (see Note 2)	±30 ±30 V					
Input voltage any input (see Notes 1 and 3)		±15	±15	V		
Voltage between any offset null terminal (N1/N2) and V _{CC} _	±0.5	±0.5	V			
Duration of output short circuit (see Note 4)	unlimited	unlimited				
Continuous total dissipation	See Dissipation Rating Table					
Operating free-air temperature range			-55 to 125	°C		
Storage temperature range			-65 to 150	°C		
Case temperature for 60 seconds FK packa			260	°C		
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds	J or W package		300	°C		
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	D or N package	260		°C		

- NOTES: 1. All voltage values, unless otherwise noted, are with respect to the midpoint between V_{CC} + and V_{CC} -.
 - 2. Differential voltages are at the noninverting input terminal with respect to the inverting input terminal.
 - 3. The magnitude of the input voltage must never exceed the magnitude of the supply voltage or 15 V, whichever is less.
 - 4. The output may be shorted to ground or either power supply. For the uA747M only, the unlimited duration of the short circuit applies at (or below) 125°C case temperature or 75°C free-air temperature.

DISSIPATION RATING TABLE

PACKAGE	$T_{\mbox{A}} \leq 25^{\circ}\mbox{C}$ POWER RATING	DERATING FACTOR	DERATE ABOVE T _A	T _A = 70°C POWER RATING	T _A = 125°C POWER RATING
D	800 mW	7.6 mW/°C	45°C	608 mW	_
FK	800 mW	11.0 mW/°C	77°C	800 mW	275 mW
J	800 mW	11.0 mW/°C	77°C	800 mW	275 mW
N	800 mW	9.2 mW/°C	63°C	736 mW	_
W	800 mW	8.0 mW/°C	50°C	640 mW	200 mW



SLOS009A - D971, FEBRUARY 1971 - REVISED OCTOBER 1990

electrical characteristics at specified free-air temperature, $V_{\mbox{CC}\pm}$ = $\pm 15~\mbox{V}$

	PARAMETER	TEST CONDITIONS	TA∓	uA747C			uA747M			UNIT	
	PARAWEIER	TEST CONDITIONS†	I AT	MIN	TYP	MAX	MIN	TYP	MAX	UNII	
VIO	Input offset voltage		25°C		1	6		1	5	>/	
	Input offset voltage	VO = 0	Full range			7.5			6	mV	
ΔV IO(adj)	Offset voltage adjust range		25°C		±15			±15		mV	
L			25°C		20	200		20	200	nA	
liO	Input offset current		Full range			300			500		
L-	Innut hine aument		25°C		80	500		80	500	A	
IIB	Input bias current		Full range			800			1500	nA	
\/	Common-mode		25°C	±12	±13		±12	±13			
VICR	input voltage range		Full range	±12			±12			V	
	Maximum peak-to-peak	R _L = 10 kΩ	25°C	24	28		24	28		V	
		R _L ≥ 10 kΩ	Full range	24			24				
VO(PP)	output voltage swing	R _L = 2 kΩ	25°C	20	26		20	26			
		$R_L \ge 2 k\Omega$	Full range	20			20				
Δ.	Large-signal differential	$R_L \ge 2 k\Omega$,	25°C	25	200		50	200		V/mV	
AVD	voltage amplification	$V_0 = \pm 10 \text{ V}$	Full range	15			25				
rį	Input resistance		25°C	0.3	2		0.3*	2		МΩ	
r _O	Output resistance	See Note 5	25°C		75			75		Ω	
Ci	Input capacitance		25°C		1.4			1.4		pF	
CMDD	Common-mode rejection ratio	V _{IC} = V _{ICR}	25°C	70	90		70	90		dB	
CMRR			Full range	70			70				
ksvs	Supply-voltage sensitivity	V _{CC} = ± 9 V to ± 15 V	25°C Full range		30	150		30	150	μV/V	
	(ΔV _{IO} / ΔV _{CC})					150			150	μνν	
los	Short-circuit output current		25°C		±25	±40		±25	±40	mA	
loo	Supply current (each amplifier)	No load	25°C		1.7	2.8		1.7	2.8	mA	
ICC			Full range			3.3			3.3		
PD	Power dissipation	No load, V _O = 0	25°C		50	85		50	85	mW	
טי	(each amplifier)	No load, $V_0 = 0$	Full range			100			100		
V_{01}/V_{02}	Channel separation		25°C		120			120	0	dB	

[†] All characteristics are measured under open-loop conditions with zero common-mode input voltage unless otherwise specified.

operating characteristics, V_{CC} $_{\pm}$ = \pm 15 V, T_A = 25°C

PARAMETER		TEST CONDITIONS	MIN	TYP	MAX	UNIT
t _r	Rise time	V: 20 mV D: 2 kO C: 400 nF Con Figure 4		0.3		μs
	Overshoot factor	$V_I = 20 \text{ mV}, R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF, See Figure 1}$		5%		
SR	Slew rate at unity gain	$V_I = 10 \text{ mV}, R_L = 2 \text{ k}\Omega, C_L = 100 \text{ pF, See Figure 1}$		0.5		V/μs

[‡] Full range for uA747C is 0°C to 70°C and for uA747M is -55°C to 125°C.

^{*}On products compliant to MIL-STD-883, Class B, this parameter is not production tested.

NOTE 5: This typical value applies only at frequencies above a few hundred hertz because of the effects of drift and thermal feedback.

PARAMETER MEASUREMENT INFORMATION

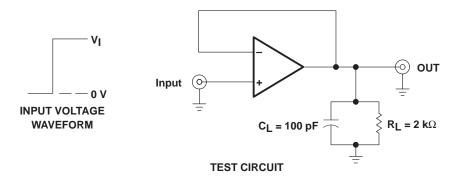


Figure 1. Rise Time, Overshoot, and Slew Rate

APPLICATION INFORMATION

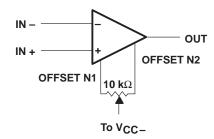


Figure 2. Input Offset Voltage Null Circuit

TYPICAL CHARACTERISTICS[†]

INPUT OFFSET CURRENT vs

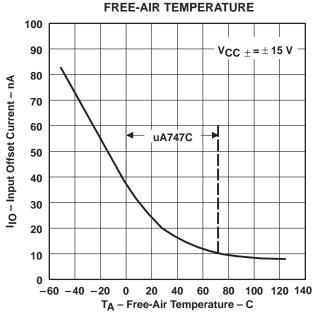


Figure 3

INPUT BIAS CURRENT

vs FREE-AIR TEMPERATURE

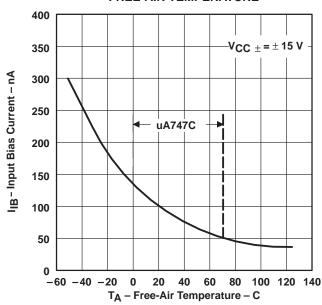


Figure 4

[†] Data at high and low temperatures are applicable only within the rated operating free-air temperature range of the particular devices.



TYPICAL CHARACTERISTICS

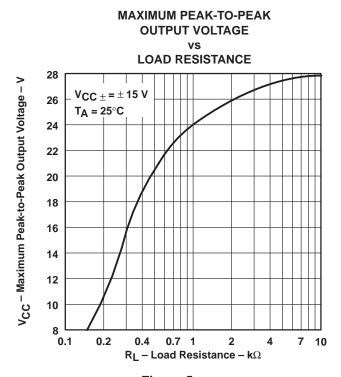


Figure 5

OPEN-LOOP LARGE-SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION VS

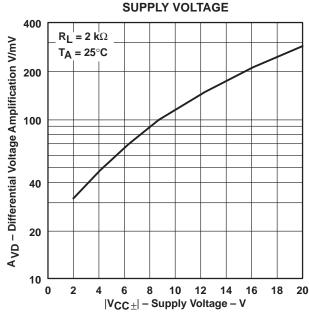


Figure 7

MAXIMUM PEAK-TO-PEAK OUTPUT VOLTAGE vs

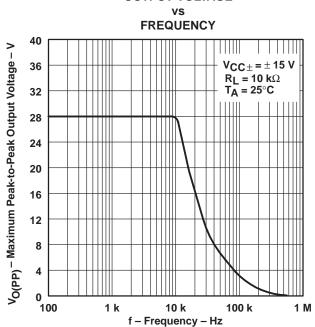


Figure 6

OPEN-LOOP LARGE-SIGNAL DIFFERENTIAL VOLTAGE AMPLIFICATION

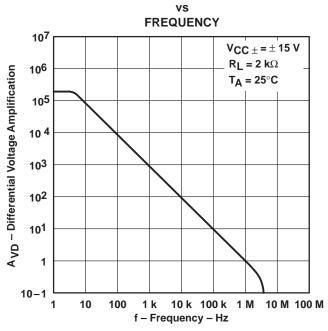
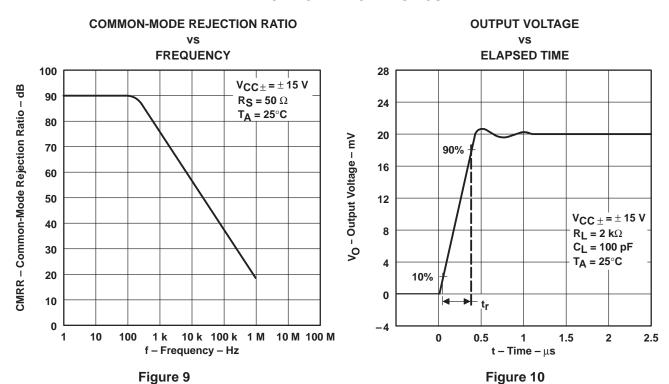


Figure 8



TYPICAL CHARACTERISTICS



VOLTAGE-FOLLOWER LARGE-SIGNAL PULSE RESPONSE

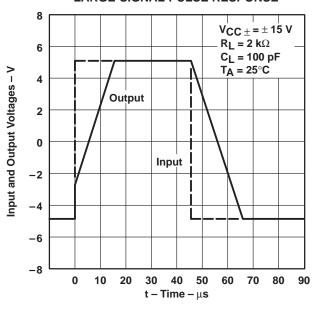


Figure 11

IMPORTANT NOTICE

Texas Instruments and its subsidiaries (TI) reserve the right to make changes to their products or to discontinue any product or service without notice, and advise customers to obtain the latest version of relevant information to verify, before placing orders, that information being relied on is current and complete. All products are sold subject to the terms and conditions of sale supplied at the time of order acknowledgement, including those pertaining to warranty, patent infringement, and limitation of liability.

TI warrants performance of its semiconductor products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are utilized to the extent TI deems necessary to support this warranty. Specific testing of all parameters of each device is not necessarily performed, except those mandated by government requirements.

CERTAIN APPLICATIONS USING SEMICONDUCTOR PRODUCTS MAY INVOLVE POTENTIAL RISKS OF DEATH, PERSONAL INJURY, OR SEVERE PROPERTY OR ENVIRONMENTAL DAMAGE ("CRITICAL APPLICATIONS"). TI SEMICONDUCTOR PRODUCTS ARE NOT DESIGNED, AUTHORIZED, OR WARRANTED TO BE SUITABLE FOR USE IN LIFE-SUPPORT DEVICES OR SYSTEMS OR OTHER CRITICAL APPLICATIONS. INCLUSION OF TI PRODUCTS IN SUCH APPLICATIONS IS UNDERSTOOD TO BE FULLY AT THE CUSTOMER'S RISK.

In order to minimize risks associated with the customer's applications, adequate design and operating safeguards must be provided by the customer to minimize inherent or procedural hazards.

TI assumes no liability for applications assistance or customer product design. TI does not warrant or represent that any license, either express or implied, is granted under any patent right, copyright, mask work right, or other intellectual property right of TI covering or relating to any combination, machine, or process in which such semiconductor products or services might be or are used. TI's publication of information regarding any third party's products or services does not constitute TI's approval, warranty or endorsement thereof.

Copyright © 1998, Texas Instruments Incorporated

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.