

isc Silicon PNP Power Transistors

TIP32A

DESCRIPTION

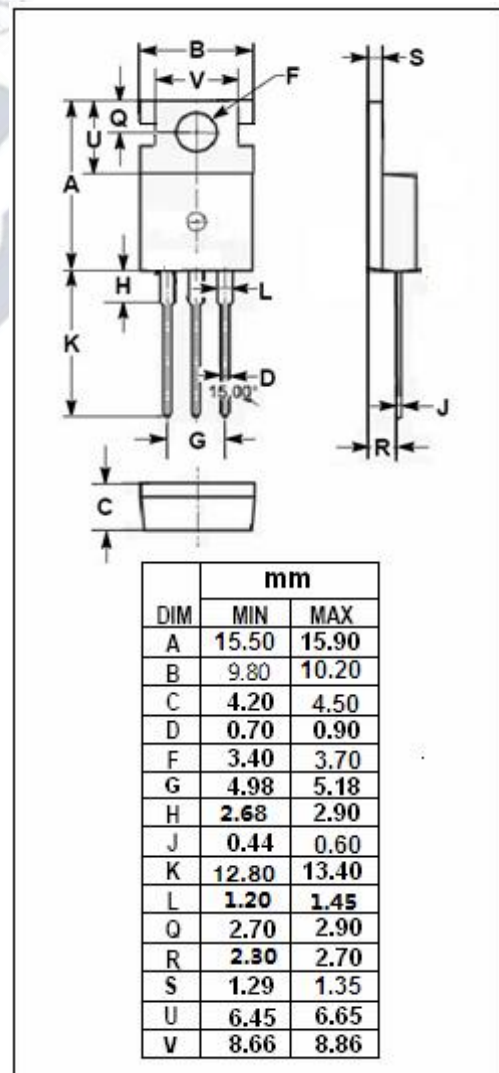
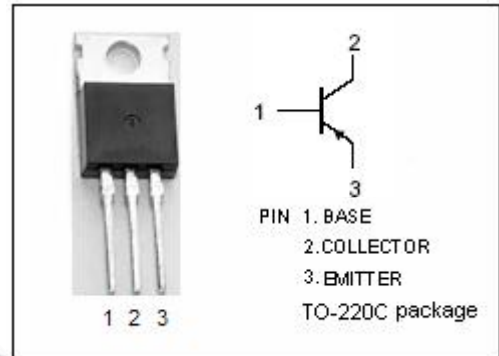
- DC Current Gain $-h_{FE} = 25(\text{Min}) @ I_C = -1.0\text{A}$
- Collector-Emitter Sustaining Voltage-
: $V_{CEO(\text{SUS})} = -60\text{V}(\text{Min})$
- Complement to Type TIP31A
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

APPLICATIONS

- Designed for use in general purpose amplifier and switching applications.

ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{CBO}	Collector-base Voltage	-60	V
V_{CEO}	Collector-emitter Voltage	-60	V
V_{EBO}	Emitter-base Voltage	-5	V
I_C	Collector Current-Continuous	-3	A
I_{CM}	Collector Current-Pulse	-5	A
I_B	Base Current	-1	A
P_C	Collector Power Dissipation $T_C=25^\circ\text{C}$	40	W
	Collector Power Dissipation $T_a=25^\circ\text{C}$	2	
T_j	Junction Temperature	150	$^\circ\text{C}$
T_{stg}	Storage Temperature Range	-65~150	$^\circ\text{C}$



isc Silicon PNP Power Transistors**TIP32A****ELECTRICAL CHARACTERISTICS** $T_C=25^{\circ}\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	MAX	UNIT
$V_{CEO(SUS)}$	Collector-Emitter Sustaining Voltage	$I_C = -30\text{mA}$; $I_B = 0$	-60		V
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = -3\text{A}$; $I_B = -0.375\text{A}$		-1.2	V
$V_{BE(on)}$	Base-Emitter On Voltage	$I_C = -3\text{A}$; $V_{CE} = -4\text{V}$		-1.8	V
I_{CES}	Collector Cutoff Current	$V_{CE} = -60\text{V}$; $V_{EB} = 0$		-0.2	mA
I_{CEO}	Collector Cutoff Current	$V_{CE} = -30\text{V}$; $I_B = 0$		-0.3	mA
I_{EBO}	Emitter Cutoff Current	$V_{EB} = -5\text{V}$; $I_C = 0$		-1.0	mA
h_{FE-1}	DC Current Gain	$I_C = -1\text{A}$; $V_{CE} = -4\text{V}$	25		
h_{FE-2}	DC Current Gain	$I_C = -3\text{A}$; $V_{CE} = -4\text{V}$	10	50	
f_T	Current-Gain—Bandwidth Product	$I_C = -0.5\text{A}$; $V_{CE} = -10\text{V}$	3		MHz