

SMLVT3V3

Low voltage Transil™

Features

- Peak pulse power 600 W (10/1000 µs)
- Stand off voltage 3.3 V
- Unidirectional type
- Low clamping factor
- Fast response time
- JEDEC registered package outline

Description

The SMLVT3V3 is a Transil diode designed specifically to protect sensitive 3.3 V equipment against transient overvoltages.

Transil diodes provide high overvoltage protection by clamping action. Their instantaneous response to transient overvoltages make them particularly suited to protect voltage sensitive devices such as MOS technology and low voltage supplied ICs.



TM: Transil is a trademark of STMicroelectronics

1 Characteristics

Table 1.	Absolute maximum ratings (T _{amb} = 25 °C)
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Symbol	Parameter	Value	Unit	
P _{PP}	Peak pulse power dissipation ⁽¹⁾	600	W	
Р	Power dissipation on infinite heatsink	T _{amb} = 50 °C	6	W
I _{FSM}	Non repetitive surge peak forward current for unidirectional types	100	A	
T _{stg}	Storage temperature range	-65 to +175	°C	
Тj	Junction temperature range	-25 to +175	°C	
Τ _L	Maximum lead temperature for soldering d	uring 10 s.	260	°C

1. For a surge greater than the maximum values, the diode will fail in short-circuit.

Table 2.Thermal resistances

Symbol	Parameter	Value	Unit
R _{th(j-l)}	Junction to leads	20	°C/W
R _{th(j-a)}	Junction to ambient on printed circuit on recommended pad layout	100	°C/W

Table 3. Electrical characteristics - parameters (T_{amb} = 25 °C)

Symbol	Parameter	1 <u>1</u>
V _{RM}	Stand-off voltage	ب <u>ا</u>
V _{BR}	Breakdown voltage	
V _{CL}	Clamping voltage	
I _{RM}	Leakage current @ VRM	$V_{cl}V_{BR}V_{RM}$ V_F V
I _{PP}	Peak pulse current	In In
αΤ	Voltage temperature coefficient	
V _F	Forward voltage drop	
R _D	Dynamic impedance	l I

Table 4. Electrical characteristics - values ($T_{amb} = 25 \ ^{\circ}C$)

	I _{RM} @V _{RM}		V _{BR} @I _R ⁽¹⁾		V _{CL} @I _{PP} 10/1000 μs		V _{CL} @I _{PP} 8/20 μs		α Τ ⁽²⁾	C ⁽³⁾
Туре	Max		Min		Max		Мах		Мах	Тур.
	μΑ	v	v	mA	v	Α	v	Α	10-4/ °C	pF
SMLVT3V3	200	3.3	4.1	1	7.3	50	10.3	200	-5.3	5200

1. Pulse test : t_p < 50 ms

2. $V_{BR} = \alpha T x (T_{amb} - 25) x V_{BR (25^{\circ}C)}$

3. $V_{R} = 0 V, F = 1 MHz$







Figure 2. Peak pulse power dissipation versus initial junction temperature (printed circuit board)





Figure 4. Clamping voltage versus peak pulse Figure 5. current (exponential waveform, maximum values)

Junction capacitance versus reverse applied voltage (typical values)









Figure 8. Relative variation of leakage current versus junction temperature



2 Order information scheme







3 Package information

- Case: JEDEC DO-214AA molded plastic over Planar junction
- Epoxy meets UL94, V0
- RoHS compliant package

In order to meet environmental requirements, ST offers these devices in different grades of ECOPACK[®] packages, depending on their level of environmental compliance. ECOPACK[®] specifications, grade definitions and product status are available at: *www.st.com*. ECOPACK[®] is an ST trademark.

Table 5. SMB Dimensions







4 Ordering information

Table 6. Ordering information

Order code	Marking	Package	Package Weight		Delivery mode	
SMLVT3V3	CD	SMB	0.12 g	2500	Tape and reel	

5 Revision history

Table 7. Document revision history

Date	Revision	Changes
Aug-2001	2	Previous issue
25-Apr-2007	3	Reformatted to current standards. Added cathode bar marker in cover page graphics and <i>Figure 11</i> .
14-Sep-2011	4	Updated Junction temperature range in <i>Table 1</i> .



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Doc ID 4146 Rev 4