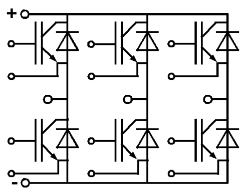
SEMITRANS® M
Low Loss IGBT Modules

SKM 75 GD 124 D

Sixpack



GD

Absolute Maximum Ratings		Values	
Symbol	**Conditions** [1]		**Units**
V_{CES}		1200	V
V_{CGR}	R_{GE} = 20 kΩ	1200	V
I_C	T_{case} = 25/60 °C	90 / 75	A
I_{CM}	T_{case} = 25/60 °C; t_p = 1 ms	180 / 150	A
V_{GES}		± 20	V
P_{tot}	per IGBT, T_{case} = 25 °C	390	W
T_j, (T_{stg})		–40 ... +150 (125)	°C
V_{isol}	AC, 1 min.	2500	V
humidity	DIN 40 040	Class F	
climate	DIN IEC 68 T.1	40/125/56	
Inverse Diode			
$I_F = –I_C$	T_{case} = 25/80 °C	75 / 50	A
$I_{FM} = –I_{CM}$	T_{case} = 25/80 °C; t_p = 1 ms	180 / 150	A
I_{FSM}	t_p = 10 ms; sin.; T_j = 150 °C	550	A
I^2t	t_p = 10 ms; T_j = 150 °C	1500	A^2s

Characteristics					
Symbol	**Conditions** [1]	**min.**	**typ.**	**max.**	**Units**
$V_{(BR)CES}$	V_{GE} = 0, I_C = 1 mA	≥ V_{CES}	–	–	V
$V_{GE(th)}$	V_{GE} = V_{CE}, I_C = 1 mA	4,5	5,5	6,5	V
I_{CES}	V_{GE} = 0, V_{CE} = V_{CES}; T_j = 25 °C	–	0,8	1	mA
	V_{GE} = 0, V_{CE} = V_{CES}; T_j = 125 °C	–	3,5	–	mA
I_{GES}	V_{GE} = 20 V, V_{CE} = 0	–	–	200	nA
V_{CEsat}	I_C = 50 A; V_{GE} = 15 V; T_j = 25 (125) °C	–	2,1(2,4)	2,45(2,85)	V
V_{CEsat}	I_C = 75 A; V_{GE} = 15 V; T_j = 25 (125) °C	–	2,5(3,0)	–	V
g_{fs}	V_{CE} = 20 V, I_C = 50 A	25	–	–	S
C_{CHC}	per IGBT	–	–	300	pF
C_{ies}	V_{GE} = 0, V_{CE} = 25 V, f = 1 MHz	–	3800	4200	pF
C_{oes}	V_{GE} = 0, V_{CE} = 25 V, f = 1 MHz	–	500	600	pF
C_{res}	V_{GE} = 0, V_{CE} = 25 V, f = 1 MHz	–	220	300	pF
L_{CE}		–	–	60	nH
$t_{d(on)}$	V_{CC} = 600 V, V_{GE} = +15 V / –15 V [3], I_C = 50 A, ind. load, R_{Gon} = R_{Goff} = 22 Ω, T_j = 125 °C	–	60	–	ns
t_r		–	55	–	ns
$t_{d(off)}$		–	420	–	ns
t_f		–	50	–	ns
E_{on}		–	8	–	mWs
E_{off}		–	6	–	mWs
Inverse Diode [8]					
$V_F = V_{EC}$	I_F = 50 A; V_{GE} = 0 V; T_j = 25 (125) °C	–	2,0(1,8)	2,5	V
$V_F = V_{EC}$	I_F = 75 A; V_{GE} = 0 V; T_j = 25 (125) °C	–	2,3(2,1)	–	V
V_{TO}	T_j = 125 °C	–	1,1	1,2	V
r_t	T_j = 125 °C	–	–	22	mΩ
I_{RRM}	I_F = 50 A; T_j = 125 °C [2]	–	39	–	A
Q_{rr}	I_F = 50 A; T_j = 125 °C [2]	–	7	–	µC
Thermal Characteristics					
R_{thjc}	per IGBT	–	–	0,32	°C/W
R_{thjc}	per diode	–	–	0,6	°C/W
R_{thch}	per module	–	–	0,05	°C/W

Features

- MOS input (voltage controlled)
- N channel, homogeneous Silicon structure (NPT- Non punch-through IGBT)
- Low loss high density chip
- Low tail current
- High short circuit capability, self limiting to 6 * I_{cnom}
- Latch-up free
- Fast & soft inverse CAL diodes [8]
- Isolated copper baseplate using DCB Direct Copper Bonding Technology without hard mould
- Large clearance (9 mm) and creepage distances (13 mm)

Typical Applications

- Switched mode power supplies
- Three phase inverters for AC motor speed control

[1] T_{case} = 25 °C, unless otherwise specified
[2] $I_F = – I_C$, V_R = 600 V, $–di_F/dt$ = 800 A/µs, V_{GE} = 0 V
[3] Use V_{GEoff} = –5... –15 V
[8] CAL = Controlled Axial Lifetime Technology

Case and mech. data → page 6

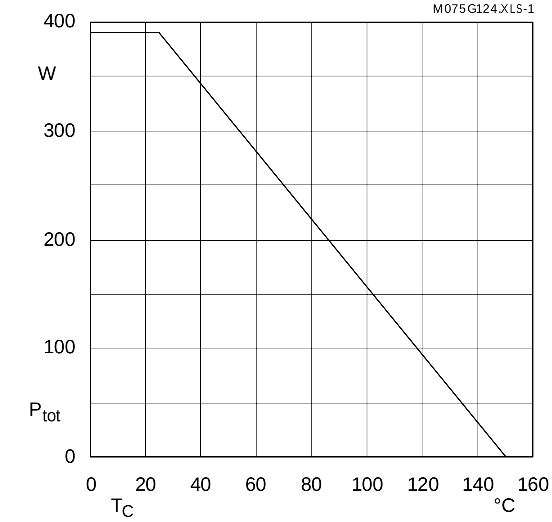


Fig. 1 Rated power dissipation $P_{tot} = f(T_C)$

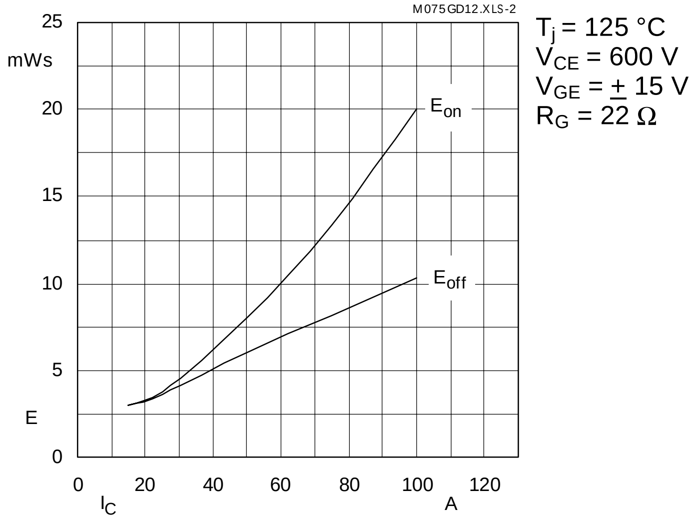


Fig. 2 Turn-on /-off energy = f (I_C)

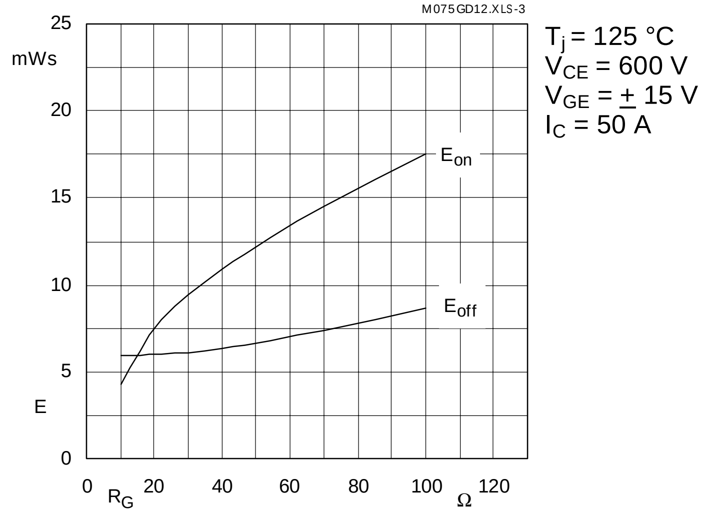


Fig. 3 Turn-on /-off energy = f (R_G)

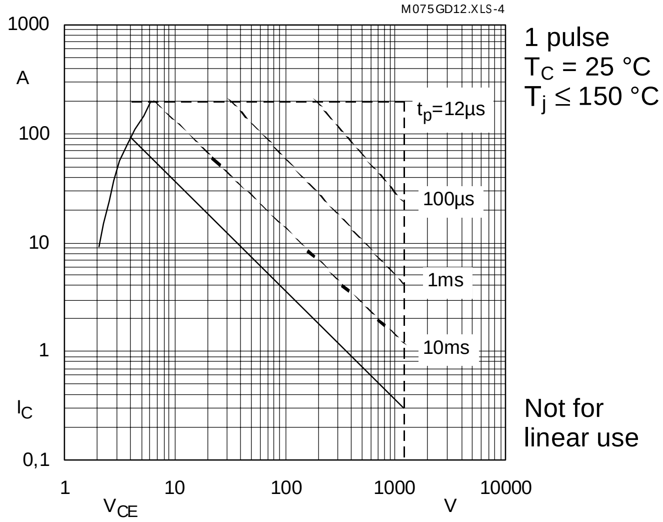


Fig. 4 Maximum safe operating area (SOA) $I_C = f(V_{CE})$

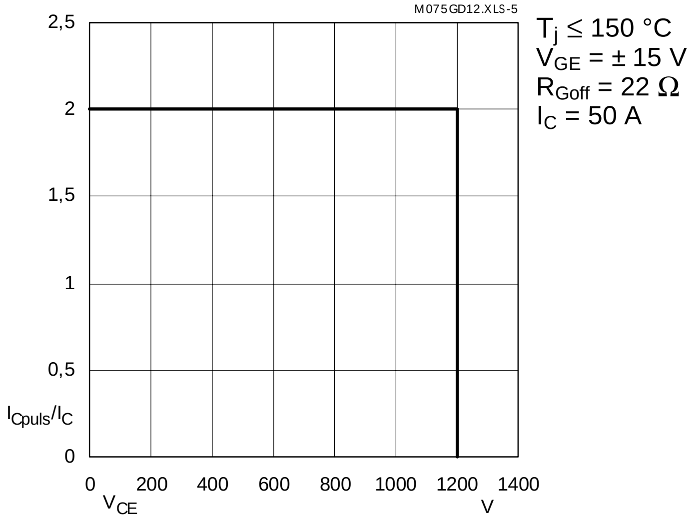


Fig. 5 Turn-off safe operating area (RBSOA)

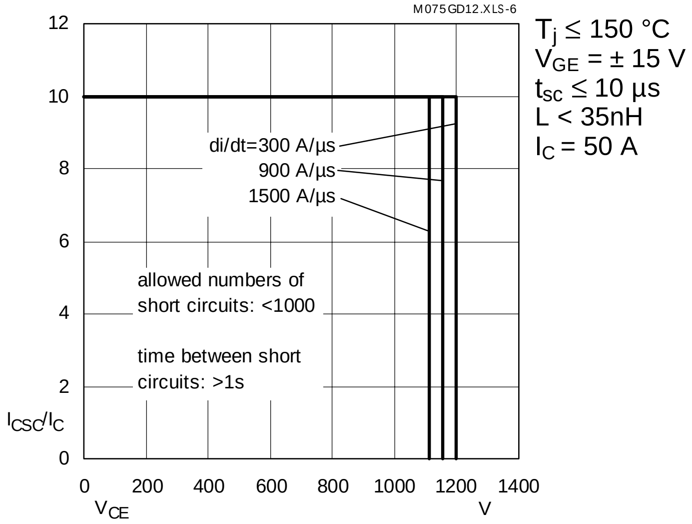


Fig. 6 Safe operating area at short circuit $I_C = f(V_{CE})$

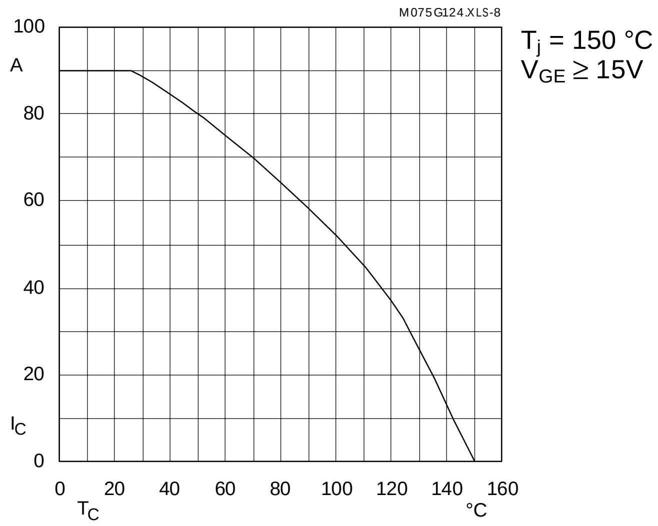


Fig. 8 Rated current vs. temperature $I_C = f(T_C)$

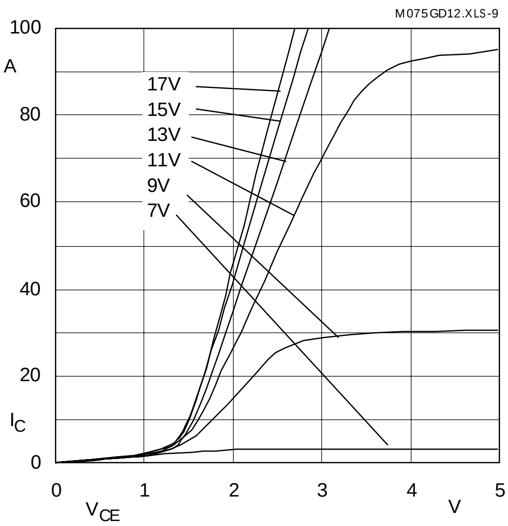


Fig. 9 Typ. output characteristic, t_p = 80 µs; 25 °C

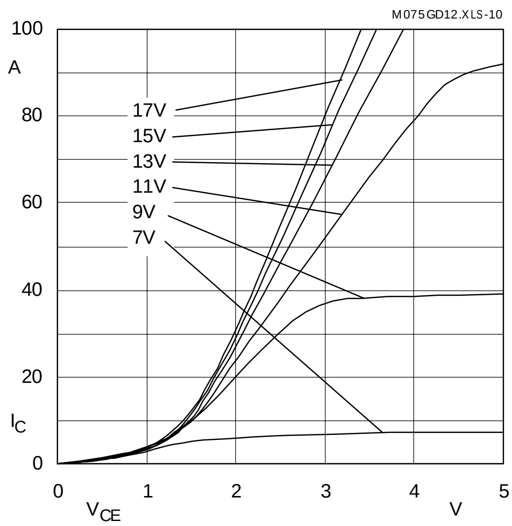


Fig. 10 Typ. output characteristic, t_p = 80 µs; 125 °C

$P_{cond(t)} = V_{CEsat(t)} \cdot I_{C(t)}$

$V_{CEsat(t)} = V_{CE(TO)(Tj)} + r_{CE(Tj)} \cdot I_{C(t)}$

$V_{CE(TO)(Tj)} \leq 1{,}30 + 0{,}0005\,(T_j - 25)$ [V]

typ.: $r_{CE(Tj)} = 0{,}018 + 0{,}00005\,(T_j - 25)$ [Ω]

max.: $r_{CE(Tj)} = 0{,}025 + 0{,}00005\,(T_j - 25)$ [Ω]

valid for $V_{GE} = +15^{+2}_{-1}$ [V]; $I_C \geq 0{,}3\, I_{Cn}$

Fig. 11 Saturation characteristic (IGBT)
Calculation elements and equations

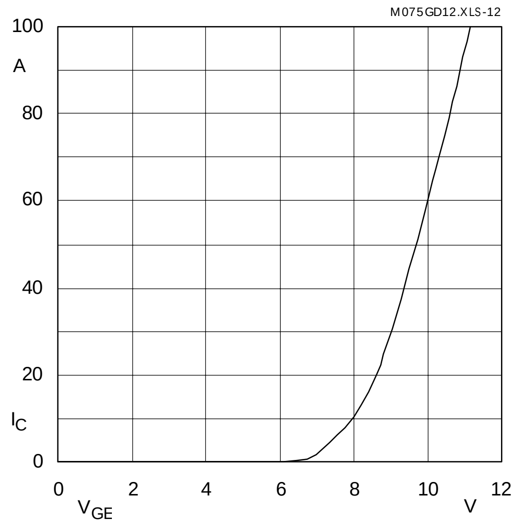


Fig. 12 Typ. transfer characteristic, t_p = 80 µs; V_{CE} = 20 V

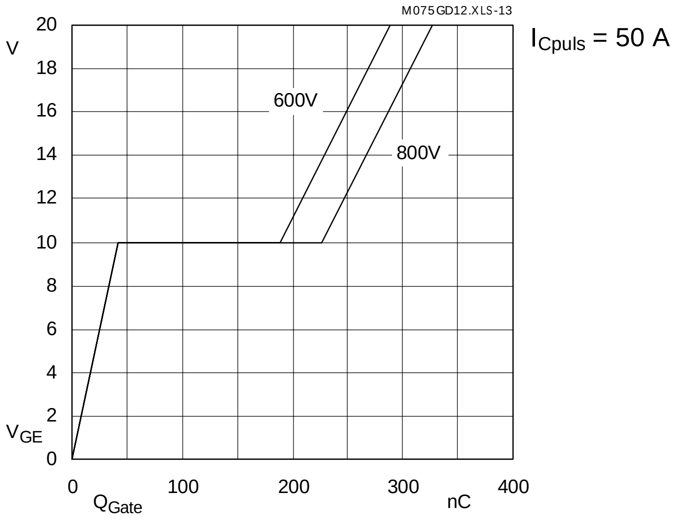


Fig. 13 Typ. gate charge characteristic

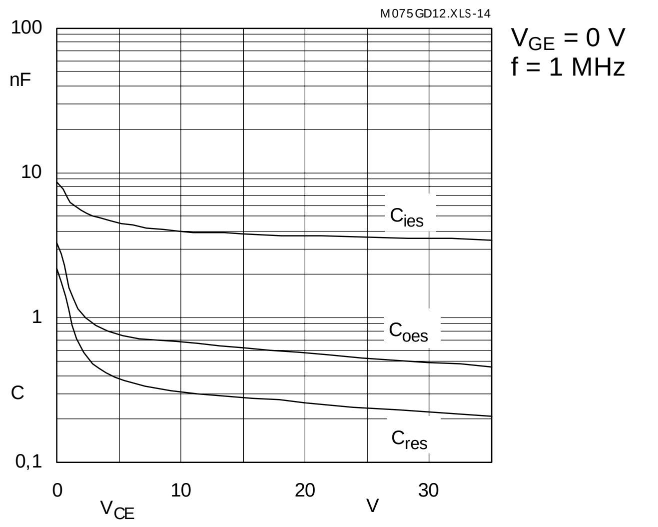


Fig. 14 Typ. capacitances vs.V_{CE}

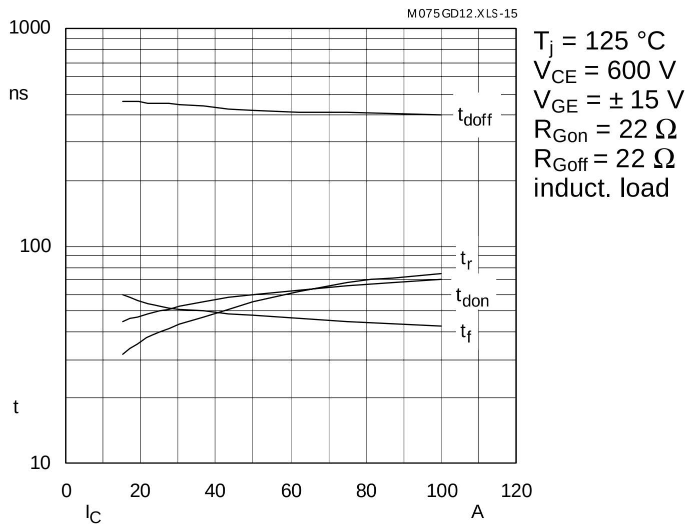


Fig. 15 Typ. switching times vs. I_C

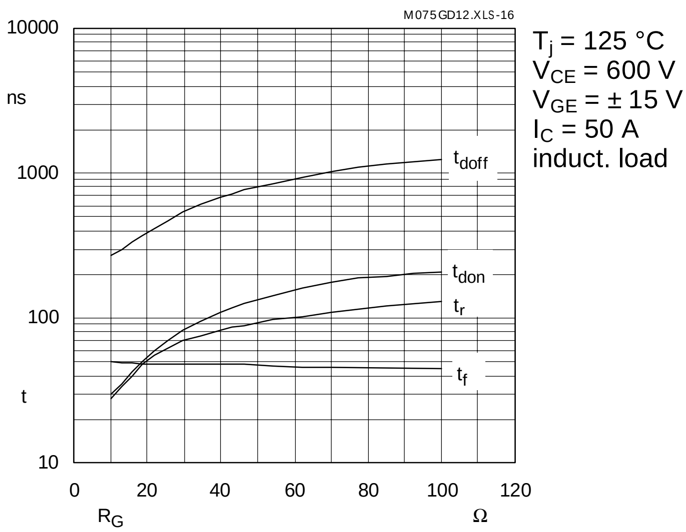


Fig. 16 Typ. switching times vs. gate resistor R_G

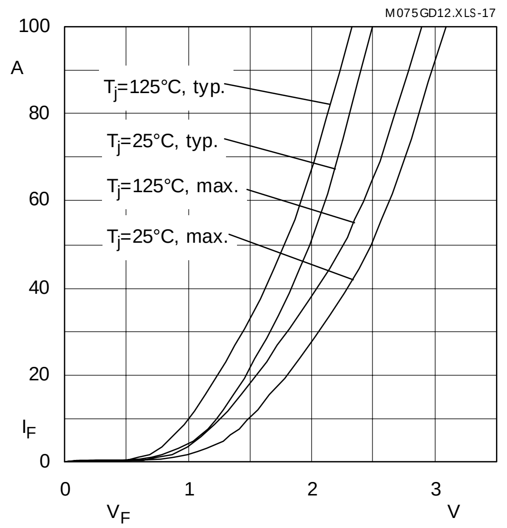


Fig. 17 Typ. CAL diode forward characteristic

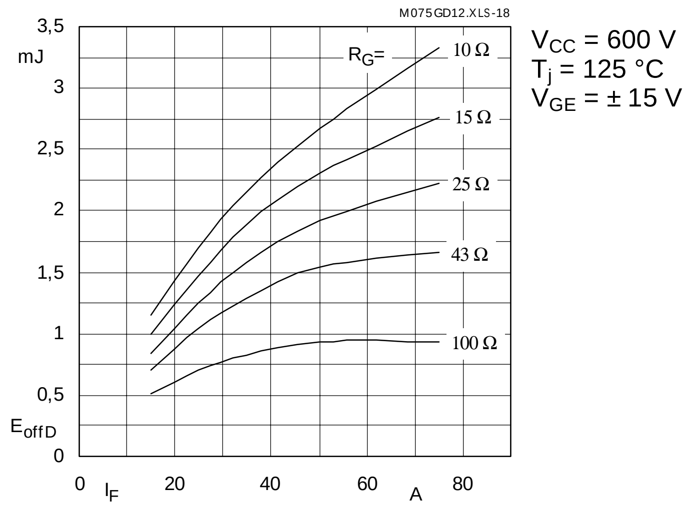


Fig. 18 Diode turn-off energy dissipation per pulse

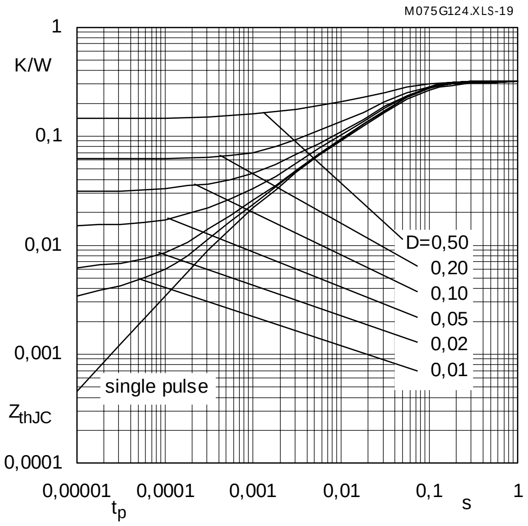


Fig. 19 Transient thermal impedance of IGBT $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$

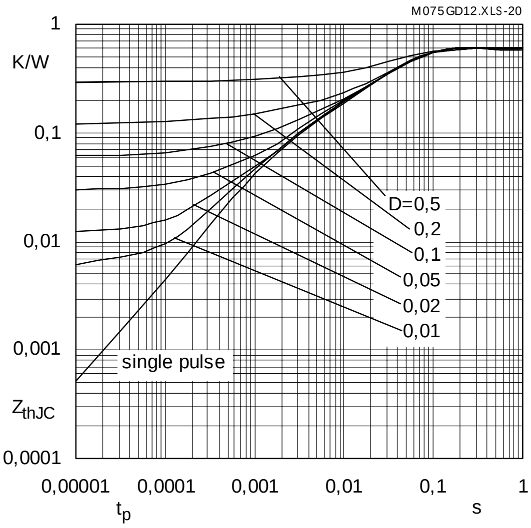
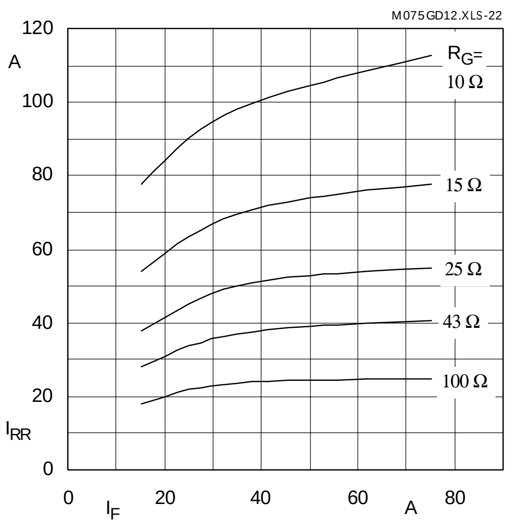
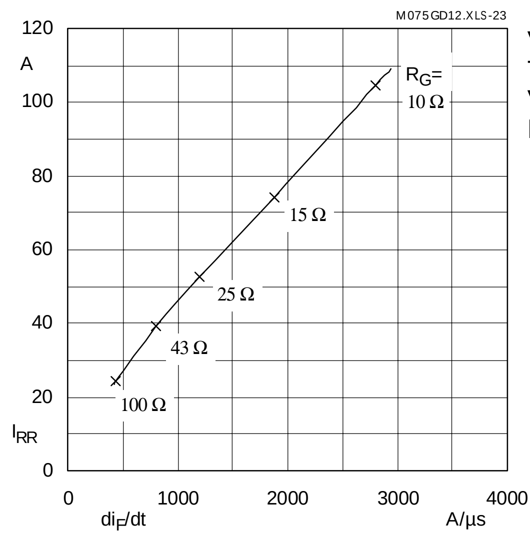


Fig. 20 Transient thermal impedance of inverse CAL diodes $Z_{thJC} = f(t_p)$; $D = t_p / t_c = t_p \cdot f$



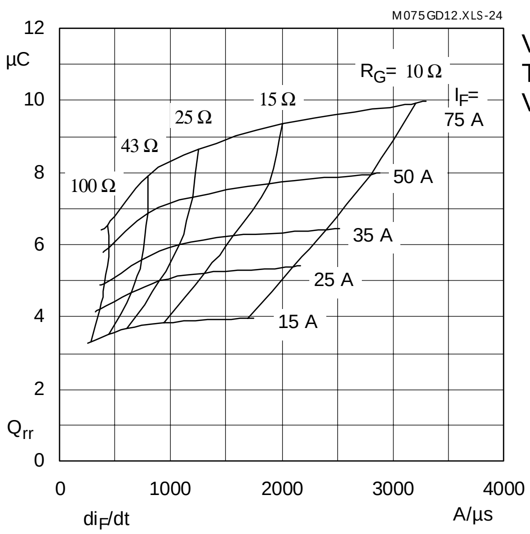
V_{CC} = 600 V
T_j = 125 °C
V_{GE} = ± 15 V

Fig. 22 Typ. CAL diode peak reverse recovery current $I_{RR} = f(I_F; R_G)$



V_{CC} = 600 V
T_j = 125 °C
V_{GE} = ± 15 V
I_F = 50 A

Fig. 23 Typ. CAL diode peak reverse recovery current $I_{RR} = f(di/dt)$

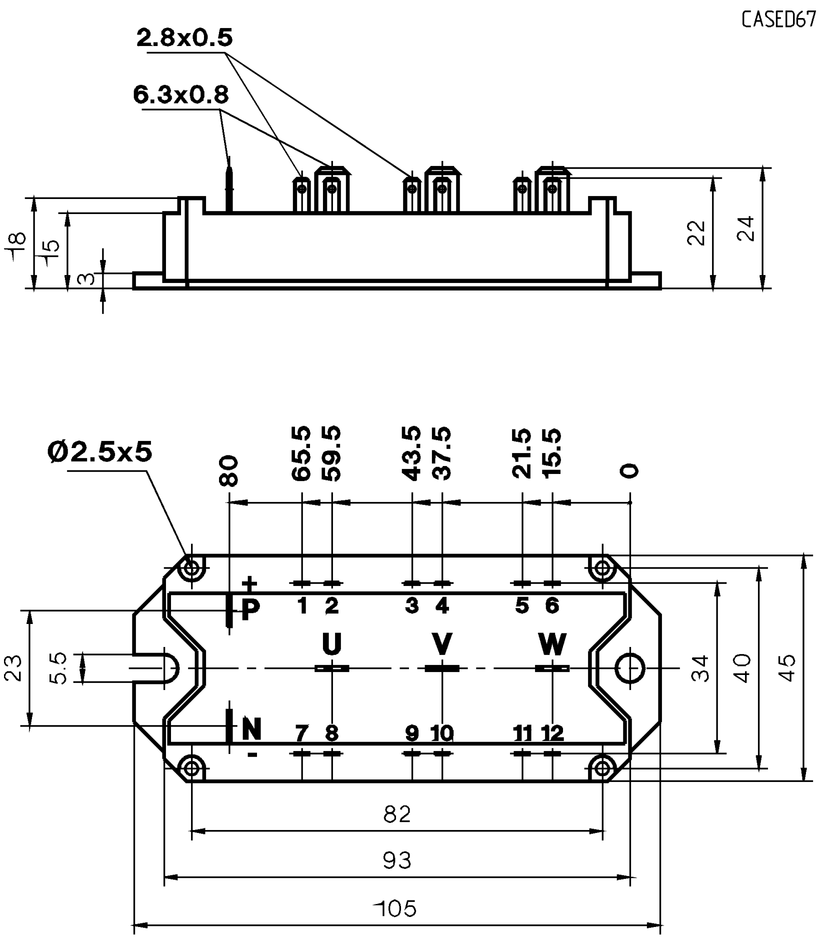


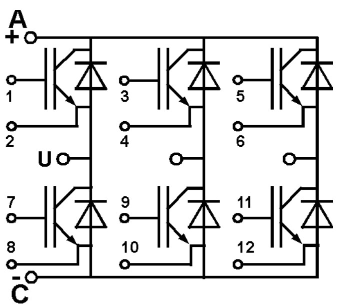
V_{CC} = 600 V
T_j = 125 °C
V_{GE} = ± 15 V

Fig. 24 Typ. CAL diode recovered charge

SEMITRANS Sixpack
Case D 67
UL Recognized
File no. E 63 532

SKM 75 GD 124 D





Dimensions in mm

Case outline and circuit diagram

Mechanical Data					
Symbol	**Conditions**	**Values**			**Units**
		min.	**typ.**	**max.**	
M_1	to heatsink, SI Units (M5)	4	–	5	Nm
	to heatsink, US Units	35	–	44	lb.in.
a		–	–	5x9,81	m/s^2
w		–	–	175	g

This is an electrostatic discharge sensitive device (ESDS). Please observe the international standard IEC 747-1, Chapter IX.

Two devices are supplied in one SEMIBOX A.
Larger packing units (10 and 20 pieces) are used if suitable SEMIBOX → page C - 1.

