

# MOC3061M, MOC3062M, MOC3063M, MOC3162M, MOC3163M 6-Pin DIP Zero-Cross Phototriac Driver Optocoupler (600 Volt Peak)

# **Features**

- Simplifies logic control of 115/240 VAC power
- Zero voltage crossing
- dv/dt of 1000V/µs guaranteed (MOC316X-M), 600V/µs guaranteed (MOC306X-M)
- VDE recognized (File # 94766)
  - ordering option V (e.g., MOC3063V-M)
- Underwriters Laboratories (UL) recognized (File #E90700, volume 2)

# **Applications**

- Solenoid/valve controls
- Static power switches
- Temperature controls
- AC motor starters
- Lighting controls
- AC motor drives
- E.M. contactors
- Solid state relays

ANODE 1

CATHODE 2

N/C 3

# **Schematic**



# Description

The MOC306XM and MOC316XM devices consist of a GaAs infrared emitting diode optically coupled to a monolithic silicon detector performing the function of a zero voltage crossing bilateral triac driver. They are designed for use with a triac in the interface of logic systems to equipment powered from 115/240 VAC lines, such as solid-state relays, industrial controls, motors, solenoids and consumer appliances, etc.

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## Absolute Maximum Ratings (T<sub>A</sub> = 25°C unless otherwise noted)

Stresses exceeding the absolute maximum ratings may damage the device. The device may not function or be operable above the recommended operating conditions and stressing the parts to these levels is not recommended. In addition, extended exposure to stresses above the recommended operating conditions may affect device reliability. The absolute maximum ratings are stress ratings only.

Symbol	Parameters	Device	Value	Units
TOTAL DEV	ICE		1	
T <sub>STG</sub>	Storage Temperature	All	-40 to +150	°C
T <sub>OPR</sub>	Operating Temperature	All	-40 to +85	°C
T <sub>SOL</sub>	Lead Solder Temperature	All	260 for 10 sec	°C
TJ	Junction Temperature Range	All	-40 to +100	°C
V <sub>ISO</sub>	Isolation Surge Voltage <sup>(1)</sup> (peak AC voltage, 60Hz, 1 sec. duration)	All	7500	Vac(pk)
PD	Total Device Power Dissipation @ 25°C Ambient	All	250	mW
	Derate above 25°C		2.94	mW/°C
EMITTER				
١ <sub>F</sub>	Continuous Forward Current	All	60	mA
V <sub>R</sub>	Reverse Voltage	All	6	V
PD	Total Power Dissipation @ 25°C Ambient	All	120	mW
	Derate above 25°C		1.41	mW/°C
DETECTOR				
V <sub>DRM</sub>	Off-State Output Terminal Voltage	All	600	V
I <sub>TSM</sub>	Peak Repetitive Surge Current (PW = 100µs, 120pps)	All	1	A
PD	Total Power Dissipation @ 25°C Ambient	All	150	mW
	Derate above 25°C		1.76	mW/°C

Note:

1. Isolation surge voltage, V<sub>ISO</sub>, is an internal device dielectric breakdown rating. For this test, Pins 1 and 2 are common, and Pins 4, 5 and 6 are common.

# Electrical Characteristics (T<sub>A</sub> = 25°C Unless otherwise specified)

### **Individual Component Characteristics**

Symbol	Parameters	Test Conditions	Device	Min.	Тур.*	Max.	Units
EMITTER	1	1	1			ļ	
V <sub>F</sub>	Input Forward Voltage	I <sub>F</sub> = 30mA	All		1.3	1.5	V
I <sub>R</sub>	Reverse Leakage Current	V <sub>R</sub> = 6V	All		0.005	100	μA
DETECTO	R						
I <sub>DRM1</sub>	Peak Blocking Current,	$V_{\text{DRM}} = 600 \text{V}, \text{ I}_{\text{F}} = 0^{(2)}$	MOC316XM		10	100	n 4
	Either Direction		MOC306XM		10	500	nA
dv/dt	Critical Rate of Rise of	I <sub>F</sub> = 0 (Figure 9) <sup>(3)</sup>	MOC306XM	600	1500		
	Off-State Voltage		MOC316XM	1000			V/µs

### **Transfer Characteristics**

Symbol	DC Characteristics	Test Conditions	Device	Min.	Тур.*	Max.	Units
I <sub>FT</sub>	LED Trigger Current	Main Terminal	MOC3061M			15	mA
	(rated I <sub>FT</sub> )	Voltage = 3V <sup>(3)</sup>	MOC3062M/ MOC3162M			10	
			MOC3063M/ MOC3163M			5	
V <sub>TM</sub>	Peak On-State Voltage, Either Direction	I <sub>TM</sub> = 100 mA peak, I <sub>F</sub> = rated I <sub>FT</sub>	All		1.8	3	V
I <sub>Н</sub>	Holding Current, Either Direction		All		500		μA

### Zero Crossing Characteristics

Symbol	Characteristics	Test Conditions	Device	Min.	Тур.*	Max.	Units
V <sub>INH</sub>	Inhibit Voltage (MT1-MT2	I <sub>F</sub> = Rated I <sub>FT</sub>	MOC3061M/2M/3M		12	20	V
	voltage above which device will not trigger)		MOC3162M/3M		12	15	
I <sub>DRM2</sub>	Leakage in Inhibited State	$I_F$ = Rated $I_{FT}$ , V <sub>DRM</sub> = 600V, off state	All		150	500	μA

### **Isolation Characteristics**

Symbol	Characteristics	Test Conditions	Device	Min.	Тур.*	Max.	Units
V <sub>ISO</sub>	Isolation Voltage	f = 60 Hz, t = 1 sec	All	7500			V

\*Typical values at  $T_A = 25^{\circ}C$ 

### Notes:

- 2. Test voltage must be applied within dv/dt rating.
- All devices are guaranteed to trigger at an I<sub>F</sub> value less than or equal to max I<sub>FT</sub>. Therefore, recommended operating I<sub>F</sub> lies between max I<sub>FT</sub> (15mA for MOC3061M, 10mA for MOC3062M & MOC3162M, 5mA for MOC3063M & MOC3163M) and absolute max I<sub>F</sub> (60mA).
- 4. This is static dv/dt. See Figure 9 for test circuit. Commutating dv/dt is a function of the load-driving thyristor(s) only.

# Safety and Insulation Ratings

As per IEC 60747-5-2, this optocoupler is suitable for "safe electrical insulation" only within the safety limit data. Compliance with the safety ratings shall be ensured by means of protective circuits.

Symbol	Parameter	Min.	Тур.	Max.	Unit
	Installation Classifications per DIN VDE 0110/1.89 Table 1				
	For Rated Main Voltage < 150Vrms		I-IV		
	For Rated Main voltage < 300Vrms		I-IV		
	Climatic Classification		55/100/21		
	Pollution Degree (DIN VDE 0110/1.89)		2		
CTI	Comparative Tracking Index	175			
V <sub>PR</sub>	Input to Output Test Voltage, Method b, $V_{IORM} \times 1.875 = V_{PR}$ , 100% Production Test with tm = 1 sec, Partial Discharge < 5pC	1594			V <sub>peak</sub>
	Input to Output Test Voltage, Method a, $V_{IORM} \times 1.5 = V_{PR}$ , Type and Sample Test with tm = 60 sec, Partial Discharge < 5pC	1275			V <sub>peak</sub>
V <sub>IORM</sub>	Max. Working Insulation Voltage	850			V <sub>peak</sub>
V <sub>IOTM</sub>	Highest Allowable Over Voltage	6000			V <sub>peak</sub>
	External Creepage	7			mm
	External Clearance	7			mm
	Insulation Thickness	0.5			mm
RIO	Insulation Resistance at Ts, $V_{IO}$ = 500V	10 <sup>9</sup>			Ω



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- 1. 100x scope probes are used, to allow high speeds and voltages.
- 2. The worst-case condition for static dv/dt is established by triggering the D.U.T. with a normal LED input current, then removing the current. The variable vernier resistor combined with various capacitor combinations allows the dv/dt to be gradually increased until the D.U.T. continues to trigger in response to the applied voltage pulse, even after the LED current has been removed. The dv/dt is then decreased until the D.U.T. stops triggering. t<sub>RC</sub> is measured at this point and recorded.



Figure 9. Circuit for Static  $\frac{dV}{dt}$  Measurement of Power Thyristors

# **Basic Applications**

Typical circuit for use when hot line switching is required. In this circuit the "hot" side of the line is switched and the load connected to the cold or neutral side. The load may be connected to either the neutral or hot line.

 $R_{in}$  is calculated so that  $I_F$  is equal to the rated  $I_{FT}$  of the part, 15mA for the MOC3061M, 10mA for the MOC3062M, or 5mA for the MOC3063M. The 39 $\Omega$  resistor and 0.01 $\mu$ F capacitor are for snubbing of the triac and is often, but not always, necessary depending upon the particular triac and load used.

Suggested method of firing two, back-to-back SCR's with a Fairchild triac driver. Diodes can be 1N4001; resistors, R1 and R2, are optional  $330\Omega$ .

### Note:

This optoisolator should not be used to drive a load directly. It is intended to be a trigger device only.









# **Ordering Information**

Option	Order Entry Identifier (Example)	Description
No option	MOC3061M	Standard Through Hole Device
S	MOC3061SM	Surface Mount Lead Bend
SR2	MOC3061SR2M	Surface Mount; Tape and Reel
Т	MOC3061TM	0.4" Lead Spacing
V	MOC3061VM	VDE 0884
TV	MOC3061TVM	VDE 0884, 0.4" Lead Spacing
SV	MOC3061SVM	VDE 0884, Surface Mount
SR2V	MOC3061SR2VM	VDE 0884, Surface Mount, Tape and Reel

# **Marking Information**



Definiti	Definitions						
1	Fairchild logo						
2	Device number						
3	VDE mark (Note: Only appears on parts ordered with VDE option – See order entry table)						
4	One digit year code, e.g., '3'						
5	Two digit work week ranging from '01' to '53'						
6	Assembly package code						

\*Note – Parts that do not have the 'V' option (see definition 3 above) that are marked with date code '325' or earlier are marked in portrait format.



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Definition of Terms							
Datasheet Identification	Product Status	Definition					
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Preliminary	First Production	Datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.					
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