- Wide Range of Supply Voltages:
  Single Supply ... 3 V to 30 V (LM2904 and LM2904Q ... 3 V to 26 V) or
   Dual Supplies
- Low Supply-Current Drain Independent of Supply Voltage ... 0.7 mA Typ
- Common-Mode Input Voltage Range Includes Ground, Allowing Direct Sensing Near Ground
- Low Input Bias and Offset Parameters:
  Input Offset Voltage ... 3 mV Typ A Versions ... 2 mV Typ
  - Input Offset Current . . . 2 nA Typ
  - Input Bias Current . . . 20 nA Typ A Versions . . . 15 nA Typ
- Differential Input Voltage Range Equal to Maximum-Rated Supply Voltage . . . ±32 V (LM2904 and LM2904Q . . . ±26 V)
- Open-Loop Differential Voltage Amplification . . . 100 V/mV Typ
- Internal Frequency Compensation

#### description/ordering information

These devices consist of two independent, high-gain, frequency-compensated operational amplifiers designed to operate from a single supply over a wide range of voltages. Operation from split supplies also is possible if the difference between the two supplies is  $3 \vee to 30 \vee (3 \vee to 26 \vee for the LM2904$  and LM2904Q), and  $V_{CC}$  is at least 1.5  $\vee$  more positive than the input common-mode voltage. The low supply-current drain is independent of the magnitude of the supply voltage.

Applications include transducer amplifiers, dc amplification blocks, and all the conventional operational amplifier circuits that now can be implemented more easily in single-supply-voltage systems. For example, these devices can be operated directly from the standard 5-V supply used in digital systems and easily provide the required interface electronics without additional ±5-V supplies.

The LM2904Q is manufactured to demanding automotive requirements.



NC - No internal connection



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

PRODUCTION DATA information is current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.



Copyright © 2002, Texas Instruments Incorporated On products compliant to MIL-PRF-38535, all parameters are tested unless otherwise noted. On all other products, production processing does not necessarily include testing of all parameters.

SLOS068E - JUNE 1976 - REVISED SEPTEMBER 2002

#### description/ordering information (continued)

T <sub>A</sub>	V <sub>IO</sub> max AT 25°C	PACK	(AGE <sup>†</sup>	ORDERABLE PART NUMBER	TOP-SIDE MARKING
		PDIP (P)	Tube	LM358P	LM358P
			Tube	LM358D	LM358
	7 mV	SOIC (D)	Tape and reel	LM358DR	LIVI358
0°C to 70°C		SOP (PS)	OP (PS) Tape and reel LM358PSR		L358
		TSSOP (PW)	Tape and reel	LM358PWR	L358
		PDIP (P)	Tube	LM358AP	LM358AP
	3 mV	SOIC (D)	Tube	LM358AD	LM358A
		30IC (D)	Tape and reel	LM358ADR	LIVI358A
		PDIP (P)	Tube	LM258P	LM258P
–25°C to 85°C	5 mV		Tube	LM258D	LM258
-25°C to 85°C		SOIC (D)	Tape and reel	LM258DR	LIVI258
	3 mV	PDIP (P)	Tube	LM258AP	LM258AP
	7 mV	PDIP (P)	Tube	LM2904P	LM2904P
			Tube	LM2904D	LM2904
			Tape and reel	LM2904DR	LIVI2904
-40°C to 125°C		SOIC (D)	Tube	LM2904QD	2904Q
			Tape and reel	LM2904QDR	2904Q
		SOP (PS)	Tape and reel	LM2904PSR	L2904
		TSSOP (PW)	Tape and reel	LM2904PWR	L2904
			Tube	LM158JG	LM158JG
	5 mV	CDIP (JG)	Tube	LM158JGB	LM158JGB
–55°C to 125°C		LCCC (FK)	Tube	LM158FKB	LM158FKB
			Tube	LM158AJG	LM158AJG
	2 mV	CDIP (JG)	Tube	LM158AJGB	LM158AJGB
		LCCC (FK)	Tube	LM158AFKB	LM158AFKB

#### **ORDERING INFORMATION**

<sup>†</sup> Package drawings, standard packing quantities, thermal data, symbolization, and PCB design guidelines are available at www.ti.com/sc/package.

#### symbol (each amplifier)





#### schematic (each amplifier)





SLOS068E – JUNE 1976 – REVISED SEPTEMBER 2002

#### absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>

		LM158, LM158A LM258, LM258A LM358, LM358A	LM2904 LM2904Q	UNIT		
Supply voltage, V <sub>CC</sub> (see Note 1)		32	26	V		
Differential input voltage, VID (see Note 2)		±32	±26	V		
Input voltage, VI (either input)	-0.3 to 32	-0.3 to 26	V			
Duration of output short circuit (one amplifier) to ground at (or below) 25°C free-air temperature (V <sub>CC</sub> $\leq$ 15 V) (see Note 3)		Unlimited	Unlimited			
Operating virtual junction temperature, TJ		150	150	°C		
	D package	97	97			
	P package	85	85	°C/W		
Package thermal impedance, $\theta_{JA}$ (see Notes 4 and 5)	PS package	95	95			
	PW package	149	Unlimited 150 97 85			
	FK package	5.61		0044		
Package thermal impedance, $\theta_{JC}$ (see Notes 6 and 7)	JG package	14.5	BA  LM2904Q    26  ±26    -0.3 to 26  Unlimited    150  97    85  95    149  149	°C/W		
	LM158, LM158A	-55 to 125				
O services from a later service T	LM258, LM258A	-25 to 85		°C		
Operating free-air temperature range, TA	LM358, LM358A	0 to 70		Ĵ		
	LM2904, LM2904Q		26 ±26 -0.3 to 26 Unlimited 150 97 85 95 149 			
Case temperature for 60 seconds	FK package	260		°C		
Lead temperature 1,6 mm (1/16 inch) from case for 60 seconds	JG package	300	300	°C		
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	P package	260	260	°C		
Storage temperature range, T <sub>stg</sub>		-65 to 150	-65 to 150	°C		

† Stresses beyond those listed under "absolute maximum ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

NOTES: 1. All voltage values, except differential voltages and V<sub>CC</sub> specified for measurement of I<sub>OS</sub>, are with respect to the network ground terminal.

- 2. Differential voltages are at IN+ with respect to IN-.
- 3. Short circuits from outputs to  $V_{CC}$  can cause excessive heating and eventual destruction.
- 4. Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JA}$ , and  $T_A$ . The maximum allowable power dissipation at any allowable ambient temperature is  $P_D = (T_J(max) T_A)/\theta_{JA}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.
- 5. The package thermal impedance is calculated in accordance with JESD 51-7.
- 6. Maximum power dissipation is a function of  $T_J(max)$ ,  $\theta_{JC}$ , and  $T_C$ . The maximum allowable power dissipation at any allowable case temperature is  $P_D = (T_J(max) T_C)/\theta_{JC}$ . Operating at the absolute maximum  $T_J$  of 150°C can affect reliability.
- 7. The package thermal impedance is calculated in accordance with MIL-STD-883.



SLOS068E - JUNE 1976 - REVISED SEPTEMBER 2002

PARAMETER		TEST CONDITIONS <sup>†</sup>		т <sub>А</sub> ‡		LM158 LM258			LM358			
					MIN	TYP§	MAX	MIN	TYP§	MAX	UNIT	
VIO	Input offset voltage	$V_{CC} = 5 V to$ $V_{IC} = V_{ICR}(n)$	MAX,	25°C		3	5		3	7	mV	
10	, ,	$V_0 = 1.4 V$	mn) <sup>,</sup>	Full range			7			9		
$\alpha_{V_{IO}}$	Average temperature coefficient of input offset voltage			Full range		7			7		μV/°C	
IIO	Input offset current	V <sub>O</sub> = 1.4 V	Vo - 14V			2	30		2	50	nA	
10		V0 V		Full range			100			150		
α <sub>IIO</sub>	Average temperature coefficient of input offset current			Full range		10			10		pA/°C	
		$\lambda = 1.4 \lambda$		25°C		-20	-150		-20	-250	nA	
IВ	Input bias current	V <sub>O</sub> = 1.4 V		Full range			-300			-500	nA	
VICR	Common-mode	V <sub>CC</sub> = 5 V to	ΜΑΧ	25°C	0 to V <sub>CC</sub> -1.5	5		0 to V <sub>CC</sub> –1.5			V	
ЧСК	input voltage range			Full range	0 to V <sub>CC</sub> –2			0 to V <sub>CC</sub> –2			v	
VOH	High-level output voltage	$R_L \ge 2 k\Omega$		25°C	V <sub>CC</sub> -1.5	5		V <sub>CC</sub> -1.5				
		$R_L \ge 10 \ k\Omega$		25°C							v	
			$R_L = 2 k\Omega$	Full range	26			26			ľ	
			$R_L \ge 10 \ k\Omega$	Full range	27	28		27	28			
V <sub>OL</sub>	Low-level output voltage	$R_L \le 10 \ k\Omega$		Full range		5	20		5	20	mV	
	Large-signal	V <sub>CC</sub> = 15 V,		25°C	50	100		25	100			
AVD	differential voltage amplification	$V_{O} = 1 V \text{ to } 1$ $R_{L} = \ge 2 k\Omega$	1 V,	Full range	25			15			V/m\	
CMRR	Common-mode rejection ratio	V <sub>CC</sub> = 5 V to V <sub>IC</sub> = V <sub>ICR</sub> (n		25°C	70	80		65	80		dB	
ksvr	Supply-voltage rejection ratio $(\Delta V_{DD}/\Delta V_{IO})$	$V_{CC} = 5 V to$	MAX	25°C	65	100		65	100		dB	
V01/V02	Crosstalk attenuation	f = 1 kHz to 2	0 kHz	25°C		120			120		dB	
		V <sub>CC</sub> = 15 V, V	√ID = 1 V,	25°C	-20	-30		-20	-30			
		$V_0 = 0$		Full range	-10			-10			mA	
lo	Output current	V <sub>CC</sub> = 15 V, V	√ID = −1 V,	25°C	10	20		10	20		mA	
		V <sub>O</sub> = 15 V		Full range	5			5				
		$V_{ID} = -1 V$ , $V_O = 200 mV$		25°C	12	30		12	30		μA	
los	Short-circuit output current	V <sub>CC</sub> at 5 V, G V <sub>O</sub> = 0	SND at –5 V,	25°C		±40	±60		±40	±60	mA	
	Supply current	V <sub>O</sub> = 2.5 V,	No load	Full range		0.7	1.2		0.7	1.2		
ICC	(two amplifiers)	V <sub>CC</sub> = MAX, No load	V <sub>O</sub> = 0.5 V,	Full range		1	2		1	2	mA	

### electrical characteristics at specified free-air temperature, V<sub>CC</sub> = 5 V (unless otherwise noted)

<sup>+</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified. MAX V<sub>CC</sub> for testing purposes is 26 V for the LM2904 and 30 V for others.

<sup>‡</sup> Full range is -55°C to 125°C for LM158, -25°C to 85°C for LM258, 0°C to 70°C for LM358, and -40°C to 125°C for LM2904 and LM2904Q. § All typical values are at  $T_A = 25$ °C.



SLOS068E - JUNE 1976 - REVISED SEPTEMBER 2002

## electrical characteristics at specified free-air temperature, $V_{CC}$ = 5 V (unless otherwise noted)

PARAMETER		PARAMETER TEST CONDITIONS <sup>†</sup>		T <sub>A</sub> ‡	LM2904 LM2904Q			UNIT
						TYP§	MAX	_
Ma		$V_{CC} = 5 V \text{ to MA}$	Χ,	25°C		3	7	
VIO	Input offset voltage	$V_{IC} = V_{ICR(min)}$	V <sub>O</sub> = 1.4 V	Full range			10	mV
$\alpha_{V_{\text{IO}}}$	Average temperature coefficient of input offset voltage			Full range		7		μV/°C
lio	Input offset current	V <sub>O</sub> = 1.4 V		25°C		2	50	nA
lio	input onset current	VO = 1.4 V		Full range			300	
$\alpha_{I_{O}}$	Average temperature coefficient of input offset current			Full range		10		pA/∘C
		V <sub>O</sub> = 1.4 V		25°C		-20	-250	~^
IВ	Input bias current	VO = 1.4 V		Full range			-500	nA
\/:op		V <sub>CC</sub> = 5 V to MAX		25°C	0 to V <sub>CC</sub> -1.5			v
VICR	Common-mode input voltage range			Full range	0 to V <sub>CC</sub> –2			
		$R_L \ge 2 \ k\Omega$		25°C				V
V <sub>OH</sub>	High-level output voltage	$R_L \ge 10 \ k\Omega$		25°C	V <sub>CC</sub> -1.5			
		V <sub>CC</sub> = MAX	$R_L = 2 k\Omega$	Full range	26			v
		VCC = MAX	$R_L \ge 10 \ k\Omega$	Full range	23	24		
V <sub>OL</sub>	Low-level output voltage	$R_L \le 10 \ k\Omega$		Full range		5	20	mV
A	Large-signal differential	$V_{CC} = 15 \text{ V}, V_{O} = 1 \text{ V} \text{ to } 11 \text{ V},$		25°C	25	100		V/m
AVD	voltage amplification	$R_L = \ge 2 k\Omega$		Full range	15			v/IIIv
CMRR	Common-mode rejection ratio	V <sub>CC</sub> = 5 V to MAX V <sub>IC</sub> = V <sub>ICR</sub> (min)	Χ,	25°C	50	80		dB
<b>k</b> SVR	Supply-voltage rejection ratio $(\Delta V_{DD}/\Delta V_{IO})$	$V_{CC} = 5 V \text{ to MA}$	x	25°C	65	100		dB
V <sub>01</sub> /V <sub>02</sub>	Crosstalk attenuation	f = 1 kHz to 20 kH	Z	25°C		120		dB
		V00 - 15 V/ V/-	- 1 \/ \/o = 0	25°C	-20	-30		
		V <sub>CC</sub> = 15 V, V <sub>ID</sub> =	= 1 v, vO = 0	Full range	-10			mA
IO	Output current	V <sub>CC</sub> = 15 V, V <sub>ID</sub> =	$-1 \sqrt{10} = 15 \sqrt{10}$	25°C	10	20		ШA
				Full range	5			
		$V_{ID} = -1 V$ ,	V <sub>O</sub> = 200 mV	25°C		30		μA
los	Short-circuit output current	V <sub>CC</sub> at 5 V, GND	at –5 V, V <sub>O</sub> = 0	25°C		±40	±60	mA
	Supply current (two amplifiers)	$V_{O} = 2.5 V$ , No load		Full range		0.7	1.2	mA
ICC		V <sub>CC</sub> = MAX, V <sub>O</sub> =	Full range		1	2	ШA	

<sup>+</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified. MAX V<sub>CC</sub> for testing purposes is 26 V for the LM2904 and 30 V for others.

<sup>‡</sup> Full range is  $-55^{\circ}$ C to  $125^{\circ}$ C for LM158,  $-25^{\circ}$ C to  $85^{\circ}$ C for LM258,  $0^{\circ}$ C to  $70^{\circ}$ C for LM358, and  $-40^{\circ}$ C to  $125^{\circ}$ C for LM2904 and LM2904Q. § All typical values are at T<sub>A</sub> =  $25^{\circ}$ C.



SLOS068E - JUNE 1976 - REVISED SEPTEMBER 2002

#### electrical characteristics at specified free-air temperature, V<sub>CC</sub> = 5 V (unless otherwise noted)

_				_ +	L	M158A		L	M258A		
P	ARAMETER	TEST CONDITIONS <sup>†</sup>		T <sub>A</sub> ‡	MIN	TYP§	MAX	MIN	TYP§	MAX	UNIT
		$V_{CC} = 5 V to$		25°C			2		2	3	
VIO	Input offset voltage	$V_{IC} = V_{ICR}(r)$ $V_{O} = 1.4 V$	nin),	Full range			4			4	mV
$\alpha_{V_{IO}}$	Average temperature coefficient of input offset voltage			Full range		7	15*		7	15	μV/°C
١O	Input offset current	V <sub>O</sub> = 1.4 V	V <sub>O</sub> = 1.4 V			2	10 30		2	15 30	nA
α <sub>I</sub> IO	Average temperature coefficient of input offset current			Full range Full range		10	200		10	200	pA/°C
1		V <sub>O</sub> = 1.4 V		25°C		-15	-50		-15	-80	nA
IВ	Input bias current	VO = 1.4 V		Full range			-100			-100	ΠA
Vien	Common-mode	$1 \sqrt{22} = 30 \sqrt{2}$		25°C	0 to V <sub>CC</sub> –1.5	5		0 to V <sub>CC</sub> –1.5	;		V
VICR	input voltage range			Full range	0 to V <sub>CC</sub> –2			0 to V <sub>CC</sub> –2			v
VOH	High-level output voltage	$R_L \ge 2 \ k\Omega$	-	25°C	V <sub>CC</sub> -1.5	5		V <sub>CC</sub> -1.5			
		V <sub>CC</sub> = 30 V	$R_L = 2 k\Omega$	Full range	26			26			V
		VCC = 30 V	$R_L \ge 10 \ k\Omega$	Full range	27	28		27	28		
V <sub>OL</sub>	Low-level output voltage	$R_L \le 10 \ k\Omega$		Full range		5	20		5	20	mV
A	Large-signal	$V_{CC} = 15 V,$	4.17	25°C	50	100		50	100		V/m∖
AVD	differential voltage amplification	$V_0 = 1 V \text{ to } 1$ $R_L = \ge 2 k\Omega$	IV,	Full range	25			25			V/IIIV
CMRR	Common-mode rejection ratio			25°C	70	80		70	80		dB
kSVR	Supply-voltage rejection ratio $(\Delta V_{DD}/\Delta V_{IO})$			25°C	65	100		65	100		dB
V <sub>01</sub> /V <sub>02</sub>	Crosstalk attenuation	f = 1 kHz to 2	0 kHz	25°C		120			120		dB
		V <sub>CC</sub> = 15 V,	V <sub>ID</sub> = 1 V,	25°C	-20	-30	-60	-20	-30	-60	
		V <sub>O</sub> = 0		Full range	-10			-10			mA
IO	Output current	V <sub>CC</sub> = 15 V,	V <sub>ID</sub> = -1 V,	25°C	10	20		10	20		
		V <sub>O</sub> = 15		Full range	5			5			
		$V_{ID} = -1 V, V_O = 200 mV$		25°C	12	30		12	30		μA
los	Short-circuit output current	V <sub>CC</sub> at 5 V, 0 V <sub>O</sub> = 0		25°C		±40	±60		±40	±60	mA
	Supply current (two	V <sub>O</sub> = 2.5 V, N		Full range		0.7	1.2		0.7	1.2	
ICC	amplifiers)	V <sub>CC</sub> = MAX, No load	V <sub>O</sub> = 0.5 V,	Full range		1	2		1	2	mA

\*On products compliant to MIL-PRF-38535, this parameter is not production tested.

<sup>+</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified. MAX V<sub>CC</sub> for testing purposes is 26 V for LM2904 and 30 V for others.

<sup>‡</sup> Full range is –55°C to 125°C for LM158A, –25°C to 85°C for LM258A, and 0°C to 70°C for LM358A.

All typical values are at T<sub>A</sub> = 25°C.



SLOS068E – JUNE 1976 – REVISED SEPTEMBER 2002

## electrical characteristics at specified free-air temperature, $V_{CC}$ = 5 V (unless otherwise noted)

	DADAMETED			_ +	L	M358A			
	PARAMETER	TEST CO	NDITIONS <sup>†</sup>	™A‡	MIN TYP§		MAX	UNIT	
Vie	Input offect voltage	$V_{CC} = 5 V \text{ to } 30$	25°C		2	3	m\/		
VIO	Input offset voltage	$V_{IC} = V_{ICR(min)}$		Full range			5	mV	
$\alpha_{V_{\text{IO}}}$	Average temperature coefficient of input offset voltage			Full range		7	20	μV/°C	
line .	Input offset current	Vo - 1 4 V				2	30	nA	
lio	input onset current	V <sub>O</sub> = 1.4 V		Full range			75	ΠA	
$\alpha_{ _{IO}}$	Average temperature coefficient of input offset current			Full range		10	300	pA/∘C	
10	Input bias current	V <sub>O</sub> = 1.4 V		25°C		-15	-100	nA	
IВ	input bias current	VO = 1.4 V		Full range			-200	ΠA	
V/				25°C	0 to V <sub>CC</sub> -1.5			V	
VICR	Common-mode input voltage range	V <sub>CC</sub> = 30 V		Full range	0 to V <sub>CC</sub> –2			v	
		$R_L \ge 2 k\Omega$		25°C	V <sub>CC</sub> -1.5				
Vон	High-level output voltage		$R_L = 2 k\Omega$	Full range	26			V	
		VCC = 30 V	$R_L \ge 10 \ k\Omega$	Full range	27	28			
V <sub>OL</sub>	Low-level output voltage	$R_L \le 10 \ k\Omega$		Full range		5	20	mV	
	Large-signal differential	V <sub>CC</sub> = 15 V, V <sub>O</sub>	= 1 V to 11 V,	25°C	25	100		1/100	
AVD	voltage amplification	$R_L = \ge 2 k\Omega$		Full range	15			V/m∖	
CMRR	Common-mode rejection ratio			25°C	65	80		dB	
k <sub>SVR</sub>	Supply-voltage rejection ratio $(\Delta V_{DD}/\Delta V_{IO})$			25°C	65	100		dB	
V <sub>01</sub> /V <sub>02</sub>	Crosstalk attenuation	f = 1 kHz to 20 kH	Ηz	25°C		120		dB	
		$V_{CC} = 15 \text{ V}, \text{ V}_{ID} = 1 \text{ V},$ $V_{O} = 0$		25°C	-20	-30	-60		
	Output current			Full range	-10			mA	
IO		$V_{CC} = 15 V, V_{ID}$	= -1 V,	25°C	10	20		ШA	
		V <sub>O</sub> = 15 V		Full range	5				
		$V_{ID} = -1 V, V_O = 200 mV$		25°C		30		μA	
IOS	Short-circuit output current	$V_{CC}$ at 5 V, GND at -5 V, V <sub>O</sub> = 0		25°C		±40	±60	mA	
		V <sub>O</sub> = 2.5 V, No load		Full range		0.7	1.2		
ICC	Supply current (two amplifiers)	V <sub>CC</sub> = MAX, V <sub>O</sub> No load	= 0.5 V	Full range		1	2	mA	

<sup>†</sup> All characteristics are measured under open-loop conditions with zero common-mode input voltage, unless otherwise specified. MAX V<sub>CC</sub> for testing purposes is 26 V for LM2904 and 30 V for others. Full range is -55°C to 125°C for LM158A, -25°C to 85°C for LM258A, and 0°C to 70°C for LM358A.

§ All typical values are at  $T_A = 25^{\circ}C$ .



SLOS068E - JUNE 1976 - REVISED SEPTEMBER 2002

## operating conditions, V\_{CC} = $\pm 15$ V, T\_A = 25°C

	PARAMETER	TEST CONDITIONS	TYP	UNIT
SR	Slew rate at unity gain	$R_L = 1 M\Omega$ , $C_L = 30 pF$ , $V_I = \pm 10 V$ (see Figure 1)	0.3	V/µs
В <sub>1</sub>	Unity-gain bandwidth	$R_L = 1 M\Omega$ , $C_L = 20 pF$ (see Figure 1)	0.7	MHz
Vn	Equivalent input noise voltage	$R_S = 100 \Omega$ , V <sub>I</sub> = 0 V, f = 1 kHz (see Figure 2)	40	nV/√Hz



Figure 1. Unity-Gain Amplifier



Figure 2. Noise-Test Circuit



#### **IMPORTANT NOTICE**

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third–party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

Mailing Address:

Texas Instruments Post Office Box 655303 Dallas, Texas 75265

Copyright © 2002, Texas Instruments Incorporated

This datasheet has been download from:

www.datasheetcatalog.com

Datasheets for electronics components.