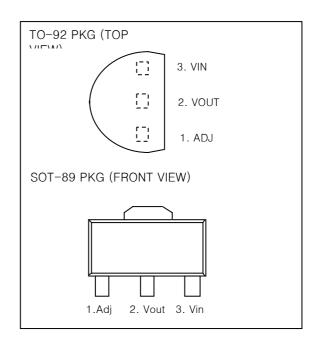
3-TERMINAL 100mA POSITIVE ADJUSTABLE REGULATOR

This monolithic integrated circuit is an adjustable 3-terminal positive voltage regulator designed to supply more than 100mA of load current with an output voltage adjustable over a 1.2 to 37V. It employs internal current limiting, thermal shut-down and safe area compensation.

FEATURES

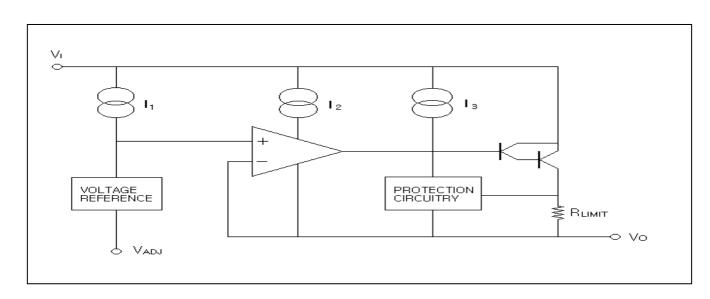
- ♦ Output current in Excess of 100mA
- Output Adjustable Between 1.2V and 37V
- ♦ Internal Thermal-Overload Protection
- ♦ Internal Short-Circuit Current-Limiting
- ♦ Output Transistor Safe-Area Compensation
- Floating operation for high voltage applications



ORDERING INFORMATION

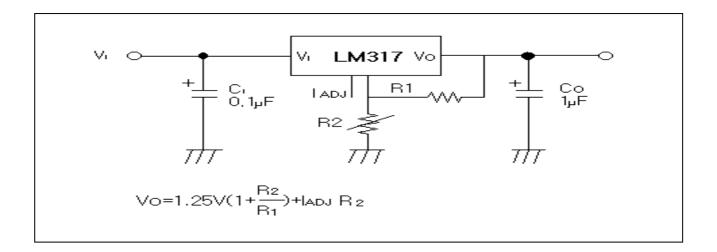
Device	Marking	Package
LM317L	LM317L	TO-92
LM317F	317	SOT-89

BLOCK DIAGRAM



TYPICAL APPLICATIONS

Fig.5 Programmable Regulator



C_i is required when regulator is located in appreciable distance from power supply filter.

Co is not needed for stability, however, it does improve transient response.

Since I_{ADJ} is controlled to less than 100μ A, the error associated with this term is negligible in most applications.

ABSOLUTE MAXIMUM RATINGS (T_A=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
Input-output Voltage Differential	V _I -V _O	40	V
Lead Temperature	TLEAD	230	°C
Power Dissipation	Po	Internally limited	_
Operating Temperature Range	Topr	0 ~ +125	°C
Storage Temperature Range	Tstg	−65 ~ +125	$^{\circ}$

ELECTRICAL CHARACTERISTICS

(V_I-V_O=5V, I_O=0.5A, 0° C \leq T_J \leq 125 $^{\circ}$ C, I_{MAX}=1.5A, P_{MAX}= 1.5 W, unless otherwise specified)

Characteristic	Symbol	Test condition		Min.	Тур.	Max.	Unit
Line Regulation	$\triangle Vo$	T _A =0 ~ 125 °C 3V≤V _I -V _O ≤40V			0.01	0.04	%/V
			3V≤Vı-Vo≤40V		0.02	0.07	%/V
		T _A =25°C, 10mA≤l _O ≤l _{MAX}					
		Vo≤5V			10	25	mV
Load Regulation	△Vo	Vo≥5V			0.1	0.5	%/Vo
		10mA≤ ₀ ≤ _{MAX}					
		Vo≤5V			20	70	mV
		Vo≥5V			0.3	1.5	%/Vo
Adjustable Pin Current	ladj				46	100	μA
Adjustable Pin Current		3∨≤∨⊢'	Vo≤40V				
Change	△ ladj	10mA≤ o≤ _{MAX}			0.2	5	μA
		P≤P _{MAX}					
		3V≤V _{IN} -V	V _{OUT} ≤40V				
Reference Voltage	VREF	10mA≤ ₀ ≤ _{MAX}		1.20	1.25	1.30	V
		P _D ≤P _{MAX}					
Temperature Stability	ST⊤				0.7		%/Vo
Minimum Load Current to	L(MIN)	V _I -V _O =40V			3.5	10	mA
Maintain Regulation							
Maximum Output Current	IO(MAX)	V⊢Vo≤5V	/, Po≤Pmax	100	200		mA
		V⊢Vo≤40V, f	P _D ≤P _{MAX} , T _{A=25} °C	0.156	0.4		
RMS Noise, % of Vout	еи	T _A =25°C, 10Hz≤f≤10KHz			0.003	0.01	%/Vo
		Vo=10V,	f=120Hz				
Ripple Rejection	RR	without CADJ			60		dB
		Cadu=10 µF		66	75		
Long-Term Stability,	ST	T _A =25℃, for end point			0.3	1	%
T _J =T _{HIGH}		measureme					

^{*} Load and line regulation are specified at constant junction temperature. Change in Vodue to heating effects must be taken into account separately. Pulse testing with low duty is used. (Pmax = 1500 mW)