		Monolithic Linear IC
	No.857C	LA7806
SANYO		B/W TV Synchronization, Deflection Circuit

The LA7806 is a multifunctional integrated circuit which is based on the internal circuit of the LA7800, incorporates various functions required for synchronization and deflection circuits of monochromatic television set, and operates on line voltage or from battery. This IC was so designed as to streamline the set by making the device more compact (DIP-16) and reducing the number of parts.

The LA7806 differs from the LA7800 in the following points.

- . No X-ray protection circuit is used.
- . The ground pins for horizontal and vertical are provided separately.
- . No horizontal regulator is used.
- . Synchronizing separation output is for vertical only.

#### Functions

. Syı	nehro	separator	. Horizontal oscillator	

- . Horizontal AFC . Vertical oscillator
- . Vertical driver

. Vertical blanking pulse making

### Features

- . Multifunction and small-size (DIP-16).
- . Minimum number of parts required.
- . Horizontal and vertical oscillators being stable to variation of ambient temperature and supply voltage owing to small warming-up drift.
- . Small variation of horizontal oscillation frequency.
- . Good linearity and interlace owing to DC bias at vertical output stage being sampling controlled within retrace time.
- . Vertical blanking pulse width being freely set up according to peripheral parts.

# Maximum Ratings at Ta=25°C

naximum Ratings at Ta=25°C			unit
Maximum Supply Voltage	$V_{12}, V_{15}$	14	v
Allowable Power Dissipation	V <sub>12</sub> ,V <sub>15</sub> Pdmax Ta=60 <sup>0</sup> C	450	mW
Operating Temperature	Topr	-20 to $+85$	öc
Storage Temperature	Tstg	-55 to +125	°Č

#### Recommended Operating Condition at Ta=25°C unit Recommended Supply Voltage V<sub>12</sub>, V<sub>15</sub> 12 V



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Note)

- 1. The vertical output circuit is shown by the basic circuit.
- 2. The peripheral parts at pin 8 should be changed in accordance with the Ver. Out circuit conditions.
- 3. The limiting resistor (220  $\Omega$  : 1Vpp) at pin 14 should be changed in proportion to the magnitude of the input video signal.
- 4. In the time constant circuit  $(120k \Omega 4.7uF)$  at pin 14, the time constant should be changed by changing the resistance value in accordance with the DC level of the input video signal and then by changing the capacitance value.

	R1	C1	R2	C2
Line operate	220k∩	0.01µF	68ka	0.068µF
Battery drive (pump-up)	220kn	0.0033µF	82k0	0.068µF

Peripheral parts at pin 8 (other applications)

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