

isc N-Channel MOSFET Transistor

IRF840LC

• FEATURES

- With low gate drive requirements
- Ultra low gate charge
- Extremely high frequency operation
- 100% avalanche tested
- Minimum Lot-to-Lot variations for robust device performance and reliable operation

• APPLICATIONS

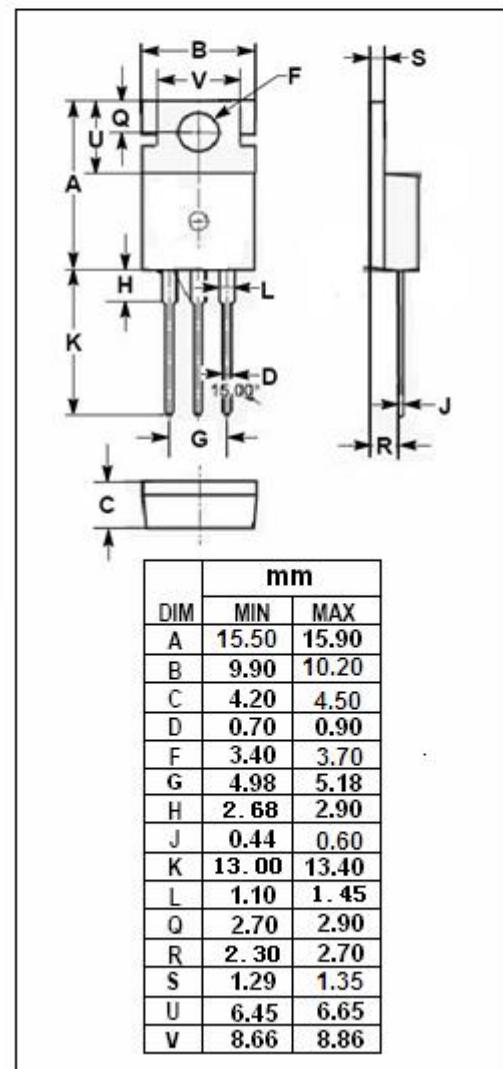
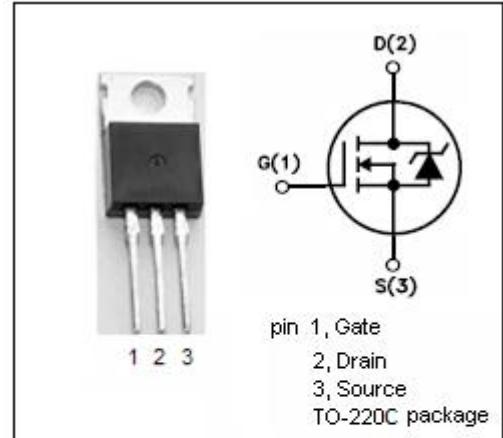
- Switching applications
- For DC-DC converter

• ABSOLUTE MAXIMUM RATINGS($T_a=25^\circ\text{C}$)

SYMBOL	PARAMETER	VALUE	UNIT
V_{DSS}	Drain-Source Voltage	500	V
V_{GSS}	Gate-Source Voltage	± 30	V
I_D	Drain Current-Continuous@ $T_c=25^\circ\text{C}$ $T_c=100^\circ\text{C}$	8.0 5.1	A
I_{DM}	Drain Current-Single Pulsed	28	A
P_D	Total Dissipation	125	W
T_j	Operating Junction Temperature	-55~150	°C
T_{stg}	Storage Temperature	-55~150	°C

• THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	MAX	UNIT
$R_{th(ch-c)}$	Channel-to-case thermal resistance	0.5	°C/W
$R_{th(ch-a)}$	Channel-to-ambient thermal resistance	62	°C/W



isc N-Channel MOSFET Transistor**IRF840LC****ELECTRICAL CHARACTERISTICS** $T_c=25^\circ\text{C}$ unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP	MAX	UNIT
BV_{DSS}	Drain-Source Breakdown Voltage	$\text{V}_{\text{GS}}=0\text{V}; \text{I}_D= 0.25\text{mA}$	500			V
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	$\text{V}_{\text{DS}}=\pm 30\text{V}; \text{I}_D=0.25\text{mA}$	2		4	V
$\text{R}_{\text{DS(on)}}$	Drain-Source On-Resistance	$\text{V}_{\text{GS}}= 10\text{V}; \text{I}_D=4.8\text{A}$			850	$\text{m}\Omega$
I_{GSS}	Gate-Source Leakage Current	$\text{V}_{\text{GS}}= \pm 30\text{V}; \text{V}_{\text{DS}}= 0\text{V}$			± 0.1	μA
I_{DSS}	Drain-Source Leakage Current	$\text{V}_{\text{DS}}= 500\text{V}; \text{V}_{\text{GS}}= 0\text{V} @ \text{T}_j=25^\circ\text{C}$ $\text{T}_j=125^\circ\text{C}$			25 250	μA
V_{SDF}	Diode forward voltage	$\text{I}_{\text{SD}}=8\text{A}, \text{V}_{\text{GS}} = 0 \text{ V}$			2.0	V