

FILTER2D-C3-SK **ANTI-INTERFERENCE FILTER**



FEATURES

- Small Footprint
- Low Temperature Rise

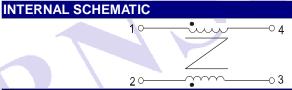
APPLICATIONS

FILTER2D-C3-SK applies to analog circuits which are noise-sensitive or EMC-sensitive occasions. If added a FILTER2D-C3-SK to the module's input, the module's output noise can be reduced substantially and EMC level can be improved.

Patent Protection RoHS

SELECTION GUIDE							
Model	Inductance value (mH)	Rated current (A)(max)	DCR(mΩ)	Weight (g)			
FILTER2D-C3-SK	5.3*2	3	180	15			

COMMON SPECIFICATIONS								
Item	Operating Conditions	Min.	Тур.	Max.	Unit			
Inductance Error Range	fo=10KHz,Uo=0.1V	-35		+35	%			
Pressure Resistance Value	Leakage Current Io=5mA			2000	VDC			
Storage Humidity			_	95	%			
Operating Temperature		-40	-	105				
Storage Temperature		-55	-	125	°C			
Soldering Temperature Resistance Of PIN	Solder Joints From Case 1.5mm for 10 seconds	4		300				
Dimension (L*W*H)		27.5*19.5*33.0		mm				
Note: All specifications measured at TA=25°C, humidity<75% unless otherwise specified.								



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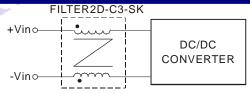
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APPLICATION CIRCUIT



IMPEDANCE CHARACTERISTICS (REFERENCE VALUE)

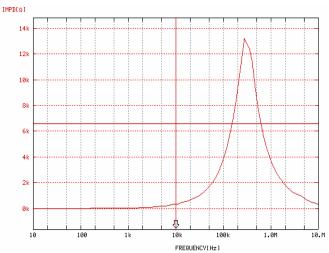


Figure 1 Impedance VS Frequency

OUTLINE DIMENSIONS

