MORNSUN®

FILTER2D-B3-SK **ANTI-INTERFERENCE FILTER**



Patent Protection RoHS

 $DCR(m\Omega)$

Weight (g)

 $^{\circ}$ C

mm

300

15.0*7.2*20.0

Rated current

(A)(max)

FEATURES

- Small Footprint
- Low Temperature Rise

APPLICATIONS

FILTER2D-B3-SK applies to analog circuits which are noise-sensitive or EMC-sensitive occasions. If added a FILTER2D-B3-SK to the module's input, the module's output noise can be reduced substantially and EMC level can be

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|---|----|---|-------|---|----|---|---|---|---|
| | | | | | | | | | |

improved.

| FILTER2D-B3-SK | 650*2 | 3 | | 40 | 3.3 | | | | | | | |
|---------------------------|--------------|--------------|------|------|------|------|--|--|--|--|--|--|
| COMMON SPECIFICATIONS | | | | | | | | | | | | |
| Item | Operating | Conditions | Min. | Тур. | Max. | Unit | | | | | | |
| Inductance ErrorRange | fo=10KHz, L | Jo=0.1V | -35 | | +35 | % | | | | | | |
| Pressure Resistance Value | e Leakage Cu | rrent Io=5mA | | | 2000 | VDC | | | | | | |
| Storage Humidity | | | | | 95 | % | | | | | | |
| Operating Temperature | | | -40 | | 105 | | | | | | | |
| Storage Temperature | | | -55 | | 125 | c | | | | | | |

Note: All specifications measured at TA=25°C, humidity<75% unless otherwise specified.

Solder joints from case

1.5mm for 10 seconds

Inductance

value (µH)

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INTERNAL SCHEMATIC

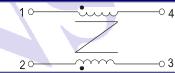
Soldering Temperature

Resistance Of PIN

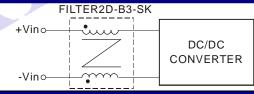
Dimension (L*W*H)

SELECTION GUIDE

Model



APPLICATION CIRCUIT



ATTENUATION SPECIFICATIONS(REFERENCE VALUE)

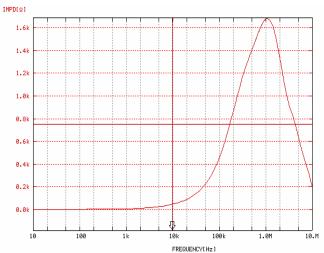


Figure 1 Impedance VS Frequency

OUTLINE DIMENSIONS

