

Silicon NPN Power Transistors

2SD2335

DESCRIPTION

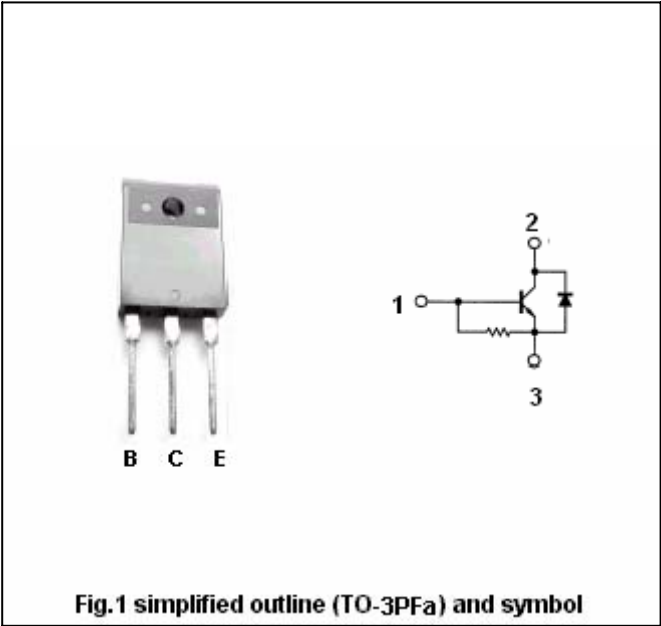
- With TO-3PFa package
- High voltage;high speed
- Built-in damper diode

APPLICATIONS

- For color TV horizontal output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector
3	Emitter



Absolute maximum ratings(Ta=25℃)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	1500	V
V_{CEO}	Collector-emitter voltage	Open base	600	V
V_{EBO}	Emitter-base voltage		5	V
I_C	Collector current		7	A
I_B	Base current		1.5	A
P_C	Collector power dissipation	$T_C=25^{\circ}C$	100	W
T_j	Junction temperature		150	℃
T_{stg}	Storage temperature		-55~150	℃

Silicon NPN Power Transistors**2SD2335****CHARACTERISTICS**T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{CEO(SUS)}	Collector-emitter sustaining voltage	I _C =100mA, I _B =0	600			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =200mA, I _C =0	5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =6A; I _B =1.2A			5.0	V
V _{BEsat}	Base-emitter saturation voltage	I _C =6A; I _B =1.2A			1.5	V
I _{CBO}	Collector cut-off current	V _{CB} =800V I _E =0			10	μ A
h _{FE}	DC current gain	I _C =1A; V _{CE} =5V	8			
V _F	Diode forward voltage	I _F =6A			2.0	V

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PACKAGE OUTLINE

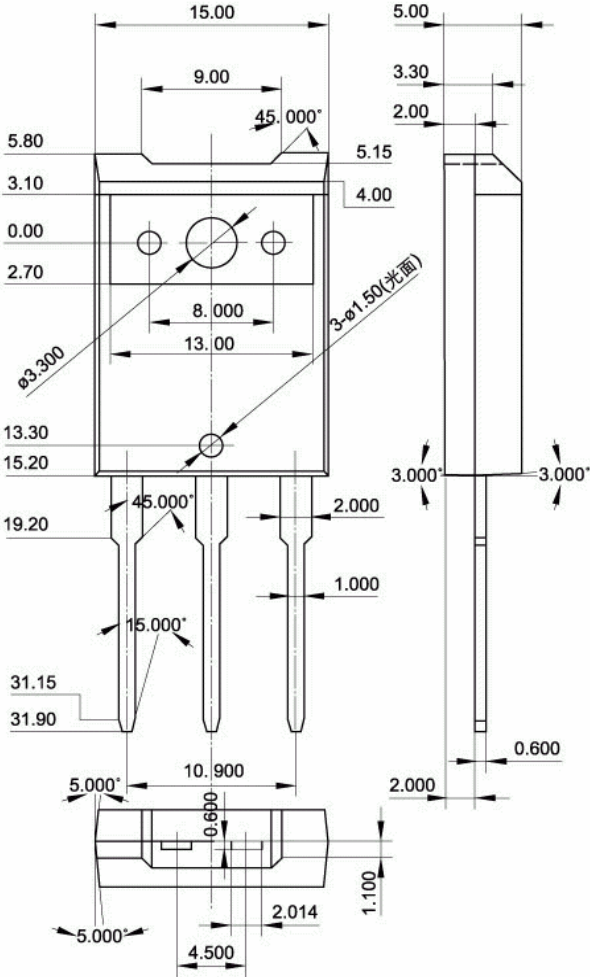


Fig.2 Outline dimensions (unindicated tolerance: ±0.30mm)