



DC COMPONENTS CO., LTD.

DISCRETE SEMICONDUCTORS

2SB564A

TECHNICAL SPECIFICATIONS OF PNP EPITAXIAL PLANAR TRANSISTOR

Description

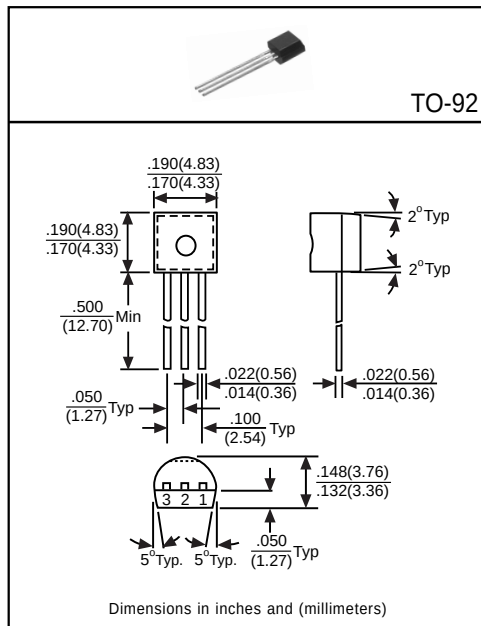
Designed for low frequency power amplifier applications.

Pinning

- 1 = Emitter
- 2 = Collector
- 3 = Base

Absolute Maximum Ratings($T_A=25^{\circ}\text{C}$)

Characteristic	Symbol	Rating	Unit
Collector-Base Voltage	V_{CB0}	-30	V
Collector-Emitter Voltage	V_{CE0}	-25	V
Emitter-Base Voltage	V_{EB0}	-5	V
Collector Current	I_C	-1	A
Total Power Dissipation	P_D	800	mW
Junction Temperature	T_J	+150	$^{\circ}\text{C}$
Storage Temperature	T_{STG}	-55 to +150	$^{\circ}\text{C}$



Electrical Characteristics

(Ratings at 25°C ambient temperature unless otherwise specified)

Characteristic	Symbol	Min	Typ	Max	Unit	Test Conditions
Collector-Base Breakdown Voltage	BV_{CB0}	-30	-	-	V	$I_C=-100\mu\text{A}$, $I_E=0$
Collector-Emitter Breakdown Voltage	BV_{CE0}	-25	-	-	V	$I_C=-10\text{mA}$, $I_B=0$
Emitter-Base Breakdown Voltage	BV_{EB0}	-5	-	-	V	$I_E=-100\mu\text{A}$, $I_C=0$
Collector Cutoff Current	I_{CBO}	-	-	-100	nA	$V_{CB}=-30\text{V}$, $I_E=0$
Collector-Emitter Saturation Voltage ⁽¹⁾	$V_{CE(sat)}$	-	-	-0.5	V	$I_C=-1\text{A}$, $I_B=-100\text{mA}$
Base-Emitter Saturation Voltage ⁽¹⁾	$V_{BE(sat)}$	-	-	-1.2	V	$I_C=-1\text{A}$, $I_B=-100\text{mA}$
DC Current Gain ⁽¹⁾	h_{FE}	70	-	400	-	$I_C=-100\text{mA}$, $V_{CE}=-1\text{V}$
Transition Frequency	f_T	-	110	-	MHz	$I_C=-10\text{mA}$, $V_{CE}=-6\text{V}$, $f=100\text{MHz}$
Output Capacitance	C_{ob}	-	18	-	pF	$V_{CB}=-6\text{V}$, $f=1\text{MHz}$

(1) Pulse Test: Pulse Width $\leq 380\mu\text{s}$, Duty Cycle $\leq 2\%$

Classification of h_{FE}

Rank	O	Y	GR
Range	70~140	120~240	200~400